

**Description** 

The MPCS-M480 series fast speed photocoupler contains a LED and photo detector in a plastic SOP5 package with built-in Schmitt trigger to provide logic-compatible waveforms, eliminating the need for additional wave shaping. The totem pole output eliminates the need for a pull up resistor and allows for direct drive Intelligent Power Module or gate drive. Minimized propagation delay difference between devices makes these optocouplers excellent solutions for improving inverter efficiency through reduced switching dead time.

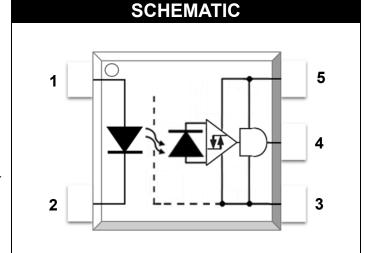
#### **Features**

- High isolation 3750 VRMS
- MSL class 1
- Guaranteed performance over temperature
   -40°C ~ +110°C.
- Totem pole output
- Truth Table Guaranteed: V<sub>CC</sub> from 4.5V to 30V
- Short Maximum Propagation Delays
- Minimized Pulse Width Distortion (PWD)
- Very High Common Mode Rejection (CMR)

### **Applications**

- IPM Interface Isolation
- Isolated IGBT/MOSFET Gate Drive
- AC and Brushless DC Motor Drives
- Industrial Inverters
- General Digital Isolation

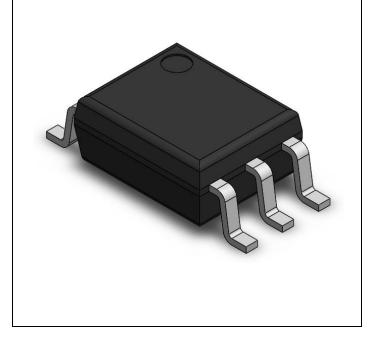
SOP5, DC Input, IPM Photo coupler



#### **PIN DEFINITION**

- 1. Anode
- 2. Cathode
- 3. GND
- **4. V**o
- **5. V**<sub>CC</sub>

### PACKAGE OUTLINE







TRUTH TABLE				
LED	OUT			
ON	Н			
OFF	L			

Note: A 0.1µF bypass capacitor must be connected between Pin 3 and 5.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	MIN.	MAX.	UNIT	NOTE
Storage Temperature	T <sub>stg</sub>	-55	125	°C	-
Operating Temperature	T <sub>opr</sub>	-40	110	°C	-
Output IC Junction Temperature	TJ	-	125	°C	-
Average Forward Input Current	l <sub>F</sub>	-	20	mA	-
Reverse Input Voltage	$V_{R}$	-	5	V	-
Output Collector Current	lo	-	50	mA	-
Supply Voltage	Vcc	0	35	V	-
Output Collector Voltage	Vo	-0.5	35	V	-
Total Package Power Dissipation	Рт	-	145	mW	-
Lead Solder Temperature	Tsol	-	260	°C	-

Note: A ceramic capacitor  $(0.1 \, \mu F)$  should be connected between pin 5 and pin 3 to stabilize the operation of a high gain linear amplifier. Otherwise, this Photocoupler may not switch properly. The bypass capacitor should be placed within 1 cm of each pin.

RECOMMENDED OPERATION CONDITIONS					
PARAMETER SYMBOL MIN. MAX. UNI					
Operating Temperature	T <sub>A</sub>	-40	110	°C	
Supply Voltage <sup>1</sup>	Vcc	4.5	30	V	
Input Current (ON) <sup>2</sup>	I <sub>F(ON)</sub>	1.6	5	mA	
Input Voltage (OFF)	V <sub>F(OFF)</sub>	-	0.8	V	

Note 1: Detector requires a  $V_{CC}$  of 4.5 V or higher for stable operation as output might be unstable if  $V_{CC}$  is lower than 4.5 V. Be sure to check the power ON/OFF operation other than the supply current.

Note 2: The initial switching threshold is 1.6 mA or less. It is recommended that 2.2 mA be used to permit at least a 20% LED degradation guard band.



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Е	ELECTRICAL OPTICAL CHARACTERISTICS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
		INP	JT CHAR	ACTERIS	STICS		
Forward Voltage	V <sub>F</sub>	1.6	2.0	2.4	V	I <sub>F</sub> = 10 mA	1
Input Reverse Voltage	BV <sub>R</sub>	5	-	-	V	IR = 10µA	-
Input Threshold Current (Low to High)	IFLH	-	0.75	1.5	mAM	VCC = 30 V, VO > 5V	-
Input Capacitance	C <sub>IN</sub>	ı	60	-	pF	VF = 0 V, f = 1 MHz	1
		OUT	PUT CHA	RACTER	ISTICS		
High Loyal Supply Current	Іссн	-	-	3.0	mA	$V_{CC} = 5.5V$ , $I_F = 5mA$ , $I_O = 0mA$	-
High Level Supply Current		-	1.8	3.0	mA	Vcc = 30V, I <sub>F</sub> = 5mA, I <sub>O</sub> =0mA	
Low Lovel Cumply Current	l	-	-	3.0	mA	Vcc = 5.5V, V <sub>F</sub> = 0V, I <sub>O</sub> =0mA	-
Low Level Supply Current	Iccl		1.8	3.0	mA	Vcc = 30V, V <sub>F</sub> = 0V, I <sub>O</sub> =0mA	
Logic High Short Circuit	la su	-	-	-100	m A	$V_{CC} = 5.5V$ , $IF = 5mA$ , $V_O = GND$	- 2
Output Current	Іоѕн	-	-	-200	mA	Vcc = 20V, IF = 5mA, Vo = GND	
Logic Low Short Circuit	l	100				V <sub>O</sub> =VCC = 5.5V, V <sub>F</sub> = 0V	2
Output Current	losL	200 IIIA	mA	Vo =VCC = 20V, V <sub>F</sub> = 0V	2		
High Level Output Voltage	Vон	Vcc -0.5	Vcc -0.07	-	V	I <sub>OL</sub> = -6.5mA	-
Low Level Output Voltage	V <sub>OL</sub>	-	0.08	0.5	V	I <sub>OL</sub> = 6.5mA	-

Note: Specified over recommended temperature ( $T_A$  = -40°C to +110°C, +4.5V  $\leq$  V<sub>CC</sub>  $\leq$  30V), I<sub>F(ON)</sub> = 1.6mA to 5mA, V<sub>F(OFF)</sub> = 0V to 0.8V, unless otherwise specified. All typicals at  $T_A$  = 25°C.

Note 1 Input capacitance is measured between pin 1 and pin 2.

Note 2: Duration of output short circuit time should not exceed 500  $\mu$ s.



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	SW	/ITCHIN	IG SPE	CIFICA	ΓΙΟΝ	_	
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
		SWITCHIN	IG CHARA	CTERISTIC	CS		
Propagation Delay Time to Output Low Level	t <sub>PHL</sub>	-	65	110	ns		1
Propagation Delay Time to Output High Level	tрLН	-	45	110	ns	f = 10kHz,	1
Pulse Width Distortion	P <sub>WD</sub>	-	20	100	ns	Duty Cycle = $50\%$ I <sub>F</sub> = $2mA$ ,	2
Propagation Delay Difference Between Any Two Parts	P <sub>DD</sub> (t <sub>PHL</sub> - t <sub>PLH</sub> )	-100	-	100	ns	Vcc = 30V	3
Rise Time	tr	-	4	-	ns		-
Fall Time	t <sub>f</sub>	ı	4	ı	ns		-
Common Mode Transient Immunity at Logic High	СМн	20	-	-	kV/μs	I <sub>F</sub> =4.0mA $V_{CC}$ = 5V, $T_A$ = 25 °C, $V_{CM}$ = 1.5kV	4
Common Mode Transient Immunity at Logic Low	CML	20	-	-	kV/μs	I <sub>F</sub> =0mA V <sub>CC</sub> = 5V,  T <sub>A</sub> = 25 °C,  V <sub>CM</sub> = 1.5kV	4

Note: Over recommended operating conditions  $T_A = -40^\circ$  C to 105° C,  $V_{CC} = +4.5$  V to 30 V,  $I_{F(ON)} = 1.6$  mA to 5 mA,  $V_{F(OFF)} = 0$  V to 0.8 V, unless otherwise specified. All typicals at  $T_A = 25^\circ$ C.

Note 1: The  $t_{PLH}$  propagation delay is measured from the 50% point on the leading edge of the input pulse to the 1.3 V point on the leading edge of the output pulse. The  $t_{PHL}$  propagation delay is measured from the 50% point on the trailing edge of the input pulse to the 1.3 V point on the trailing edge of the output pulse.

Note 2: Pulse Width Distortion (PWD) is defined as |t<sub>PHL</sub> - t<sub>PLH</sub> | for any given device.

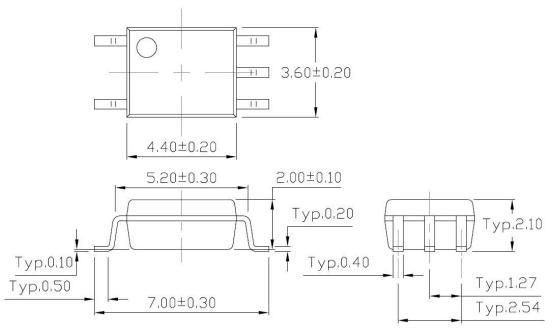
Note 3: The difference of t<sub>PLH</sub> and t<sub>PHL</sub> between any two devices under the same test condition.

Note 4: CMH is the maximum slew rate of the common mode voltage that can be sustained with the output voltage in the logic high state,  $V_O > 2.0$  V. CML is the maximum slew rate of the common mode voltage that can be sustained with the output voltage in the logic low state,  $V_O < 0.8$  V. Note: Equal value split resistors (Rin/2) must be used at both ends of the LED.

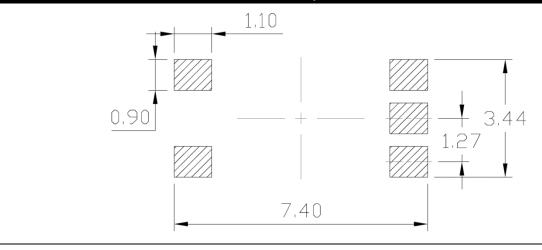




## PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)



### RECOMMENDED SOLDER MASK (Dimensions in mm unless otherwise stated)







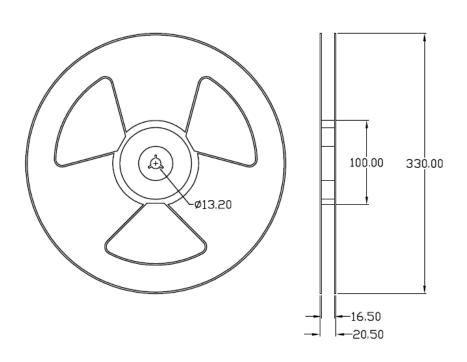
# SOP5, DC Input, IPM Photo coupler CARRIER TAPE SPECIFICATIONS (Dimensions in mm unless otherwise stated) **Option T1 -**-2.00 - Ø1,50 4.00 --1.75 7.50 16.00 -8,00 **Option T2** <del>-</del> 2.00 Ø1.50 4.00 --1.75 7,50 16.00 -2.48 -8,00

Rev: 0.1(Preliminary)

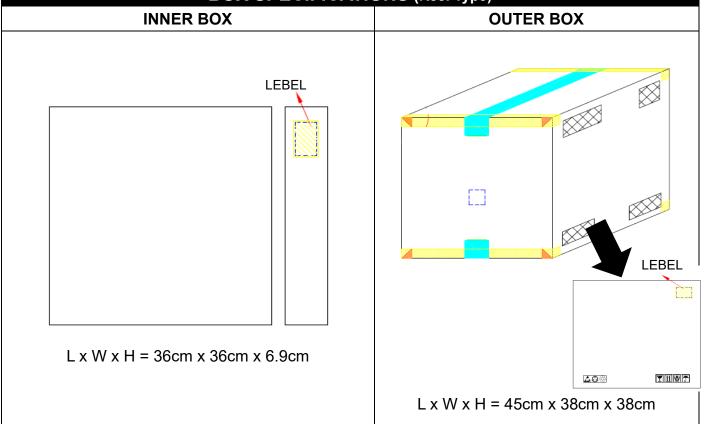
Release Date: 2024/11/12



## REEL SPECIFICATIONS (Dimensions in mm unless otherwise stated)



### BOX SPECIFICATIONS (Reel Type)





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## ORDERING AND MARKING INFORMATION

#### **MARKING INFORMATION**



M : Company Abbr.

YY : Year date code

WW : 2-digit work week M480 : Part Number

T : Factory identification markV :VDE Identification(Option)

#### **ORDERING INFORMATION**

# MPCS-M480(Z)-GV

MPC - Company Abbr.

S – Stack

M480 - Part Number

Z – Tape and Reel Option (T1/T2)

G - Green Part

V –VDE Option (V or None)

#### LABEL INFORMATION



#### 喆光照明光電股份有限公司

WISELITE Optronics Co., Ltd

Part No: XXXXXXXXXXXXXX

Bin Code : X



Lot No: XXXXXXXXXX

Date Code : XXXX Q'ty : XXXX pcs





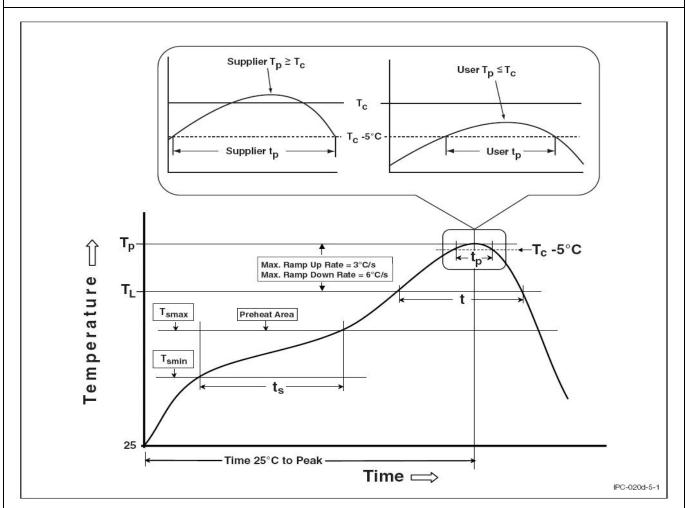
#### **PACKING QUANTITY**

Option	Quantity	Quantity – Inner	Quantity – Outer box
		box	
T1	3000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 45k Units
T2	3000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 45k Units



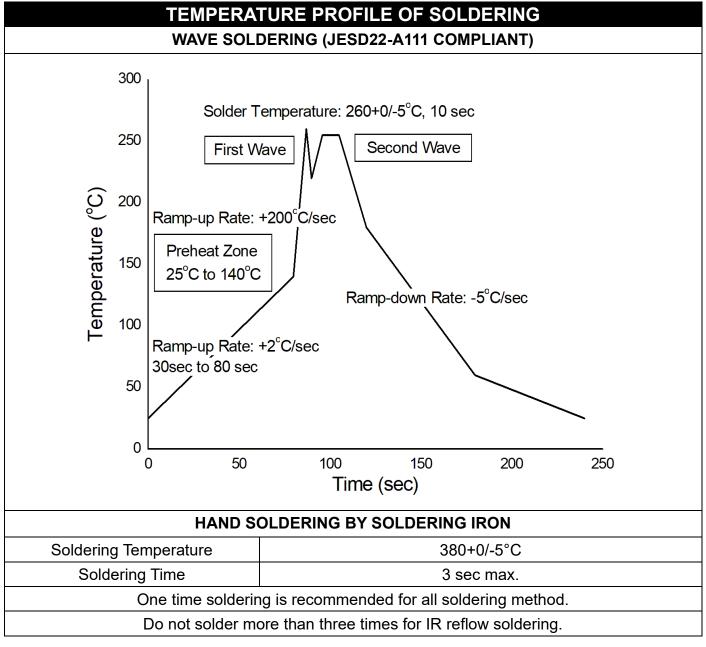
# REFLOW INFORMATION





Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile	
Temperature Min. (Tsmin)	100°C	150°C	
Temperature Max. (Tsmax)	150°C	200°C	
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds	
Ramp-up Rate (tL to tP)	3°C/second max.	3°C/second max.	
Liquidous Temperature (TL)	183°C	217°C	
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds	
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C	
Time (tP) within 5°C of 260°C	20 seconds	30 seconds	
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max	
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.	









#### **DISCLAIMER**

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