

LSOP6, DC Input, High Speed 1MBit/s Optocoupler

Description

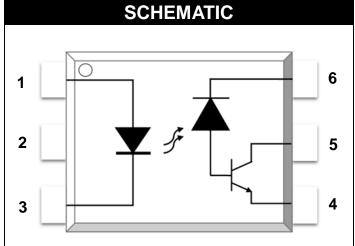
The MPCS-50L series consists of a high efficient AIGaP Light Emitting Diode and a high speed optical detector. This design provides excellent AC and DC isolation between the input and output sides of the Optocoupler. The output of the optical detector features an open collector Schottky clamped transistor. The internal shield ensures high common mode transient immunity. A guaranteed common mode transient immunity is up to 15KV/µs (min.)

Features

- High speed 1MBd typical
- Very high common mode transient immunity: 15K V/µs at VCM = 1500 V guaranteed
- Guarantee performance from temperature range: -40°C to 110°C
- TTL compatible and Open collector output
- Regulatory Approvals
 - UL UL1577
 - VDE EN60747-5-5(VDE0884-5)
 - CQC GB4943.1, GB8898

Applications

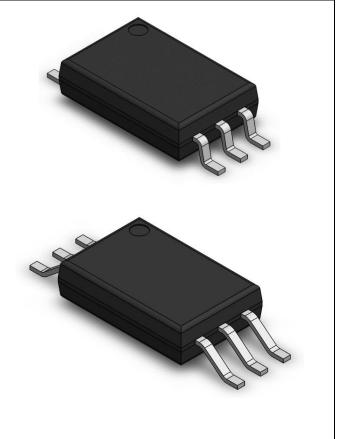
- Digital signal isolation
- Communications interface
- Micro-controller interface
- Feedback elements in switching power supplies
- Digital isolation for A/D, D/A conversion Digital field



PIN DEFINITION

1. Anode	6. Vcc
2. NC	5. Vo
3. Cathode	4. GND

PACKAGE OUTLINE





ω –	LSOP	5, DC Input,	, High Spee	d 1MBit/s	Optocoupler				
ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	MIN.	MAX.	UNIT	NOTE				
Storage Temperature	T _{stg}	-55	125	°C	-				
Operating Temperature	T_{opr}	-40	110	°C	-				
Supply Voltage	Vcc	-0.5	30	V	-				
Average Forward Input Current	lF	-	25	mA	-				
Reverse Input Voltage	V _R	-	5	V	-				
Input Power Dissipation	Ρı	-	45	mW	-				
Output Collector Current	lo	-	8	mA	-				
Output Collector Voltage	Vo	-0.5	20	V	-				
Output Collector Power Dissipation	Po	-	100	mW	-				
Lead Solder Temperature	T _{sol}	-	260	°C	-				

Note: Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.



LSOP6, DC Input, High Speed 1MBit/s Optocouple								
	ELECT	RICAL	OPTIC	CAL CH	IARAC	TERISTICS		
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE	
		11	NPUT CH	ARACTEF	RISTICS			
Input Forward Voltage	VF	1.6	2.0	2.4	V	IF =16mA, TA=25 °C	-	
Input Reverse Voltage	BVR	5	-	-	V	IR = 10μΑ	-	
		20	100			IF = 16mA; VCC = 4.5V;	4	
Current transfer ratio	CTR	20	100	-	%	TA = 25 °C; VO = 0.4V		
	CIR	15	110		70	IF = 16mA; VCC = 4.5V;	1	
			110	-		TA = 25°C; VO = 0.5V		
	Vol	-	0.1	0.4	V	IF = 16mA;VCC = 4.5V;	-	
Logic low output voltage						lo = 3.0mA; TA = 25°C		
output voltage		-	-	0.5		IF = 16mA;VCC = 4.5V;		
						lo = 2.4mA; TA = 25°C	-	
		-	0.002	0.5		IF = 0mA, VO = VCC = 5.5V,		
						TA = 25°C	-	
Logic high output current	Іон		0.013	1		IF = 0mA, VO = VCC = 15V		
		-				TA = 25°C	-	
		-	-	50	μA	TA = 0 ~ 70°C		
Logic low supply current		lcc∟ - 2		-		IF = 16mA, Vo = open		
	ICCL		230			(VCC=30V)		
Logic high supply	1		0.000	4		IF = 0mA, Vo = open ;		
current	Іссн	-	0.002	0.002 1		TA = 25°C (VCC = 30V)		

Note: All Typical values at TA = 25°C unless otherwise specified. All minimum and maximum specifications are at recommended operating condition.

Note1: Current Transfer Ratio in percent is defined as the ratio of output collector current, IO, to the forward LED input current, IF, times 100%.



\mathbf{C}	LSOP6, DC Input, High Speed 1MBit/s Optocoupler								
SWITCHING SPECIFICATION									
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION N			
Drana setien Delay	4	-	250	800		TA = 25°C		2	
Propagation Delay Time to	t _{PHL}	-	-	800	ns -	0 ~ 100°C	RL=1.9KΩ	2	
	4	-	650	800		TA = 25°C		1	
Low Output Level	t _{PLH}	-	-	800		0 ~ 100°C		1	
Logic High						IF = 0mA;VCM =	= 1500Vp-p;		
Common Mode	CM _H	15	25	-	KV/µs	CL = 15 pF; TA=25°C, RL=1.9KΩ		3	
Transient Immunity									
Logic Low						IF = 16mA;VCM	= 1500Vp-p		
Common Mode	CM∟	15	25	-	KV/µs	CL = 15 pF; T/	A = 25°C ,	4	
Transient Immunity						RL = 1.9	ϿΚΩ		

Note: All Typical values at TA = 25°C unless otherwise specified. All minimum and maximum specifications are at recommended operating condition.

Note 1: t_{PLH} (propagation delay) is measured from the 3.75 mA point on the falling edge of the input pulse to the 1.5 V point on the rising edge of the output pulse.

Note 2: t_{PHL} (propagation delay) is measured from the 3.75 mA point on the rising edge of the input pulse to the 1.5 V point on the falling edge of the output pulse.

Note 3: CM_H is the maximum tolerable rate of rise of the common mode voltage to assure that the output will remain in a high logic state.

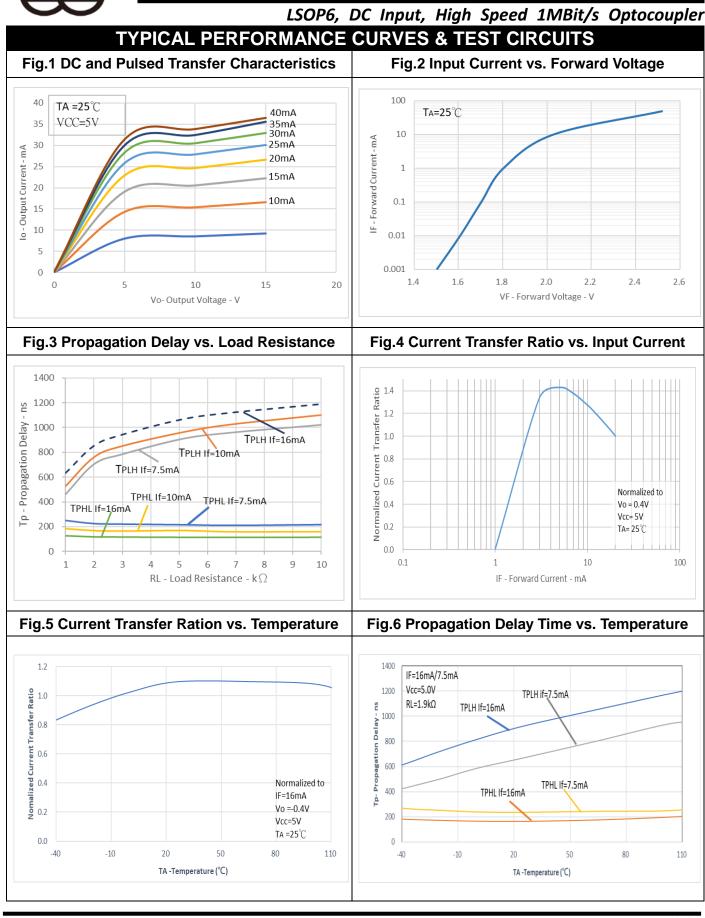
Note 4: CM_L is the maximum tolerable rate of fall of the common mode voltage to assure that the output will remain in a low logic state.

ISOLATION CHARACTERISTIC								
PARAMETER	SYMBOL	DEVICE	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
Withstand Insulation		MPCS-50LP				V	RH ≤ 40%-60%,	1.0
Test Voltage	Viso	MPCS-50LW	5000	-	-	V	t = 1min, T _A = 25 °C	1,2
Input-Output	D			4.012		0		
Resistance	R⊦o	-	-	10 ¹²	-	Ω	V _{I-O} = 500V DC	I

Note: All Typical values at TA = 25°C unless otherwise specified. All minimum and maximum specifications are at recommended operating condition.

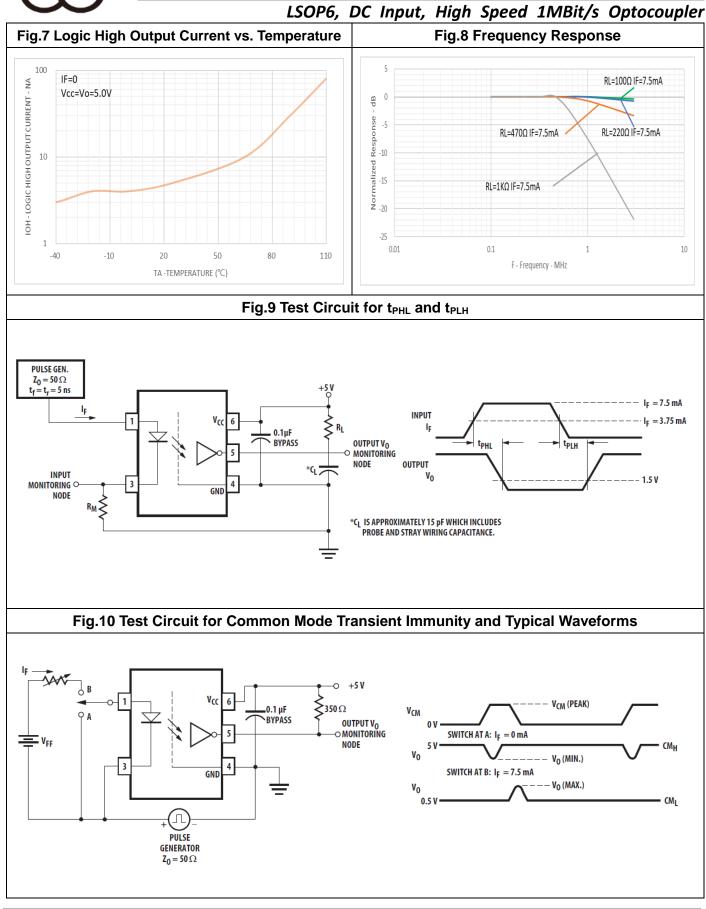
Note 1: Device is considered a two-terminal device: pins 1, 2, 3 shorted together, and pins 4, 5, 6 shorted together.

Note 2: In accordance with UL1577, each optocoupler is proof tested by applying an insulation test voltage 6000 Vrms for one second (leakage current less than 10 uA).



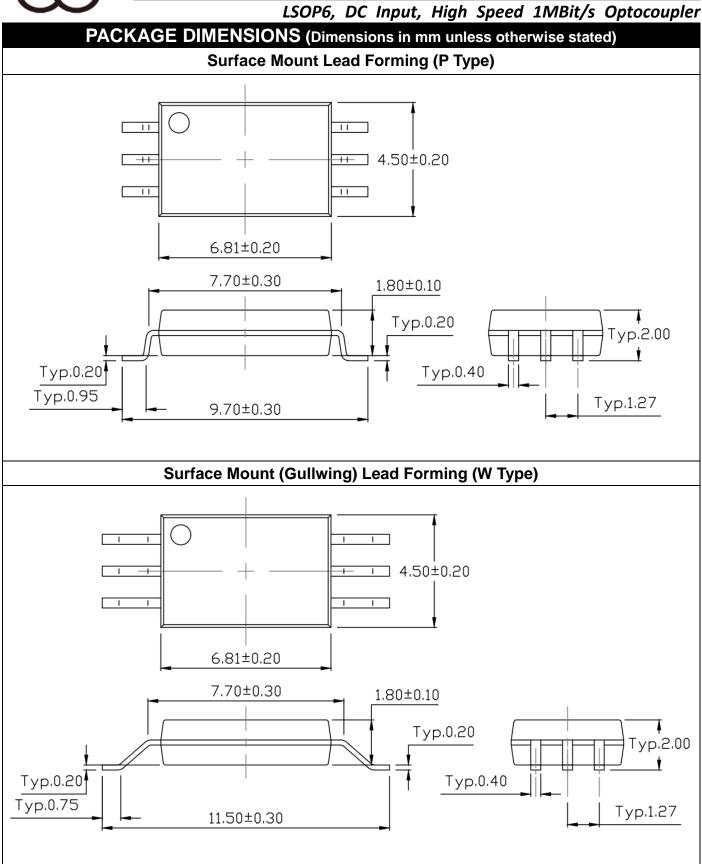
Release Date: 2024/4/12



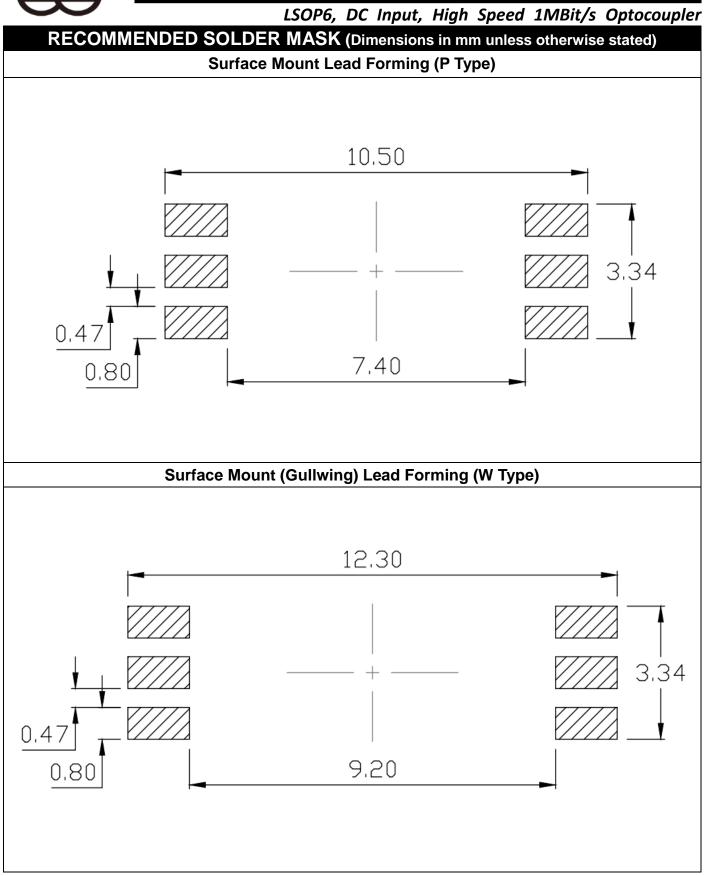


Release Date: 2024/4/12

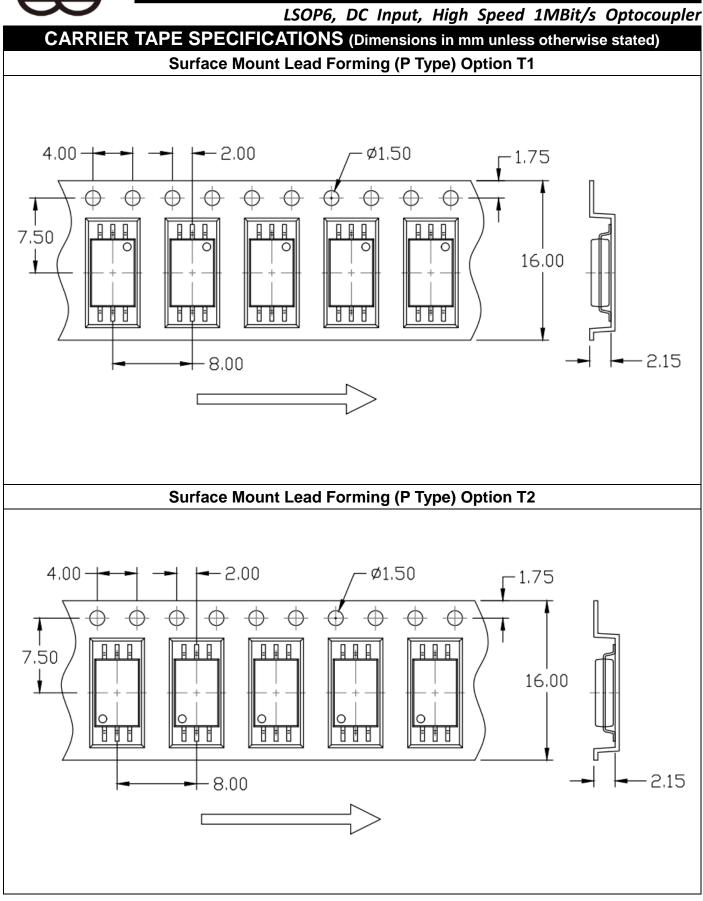




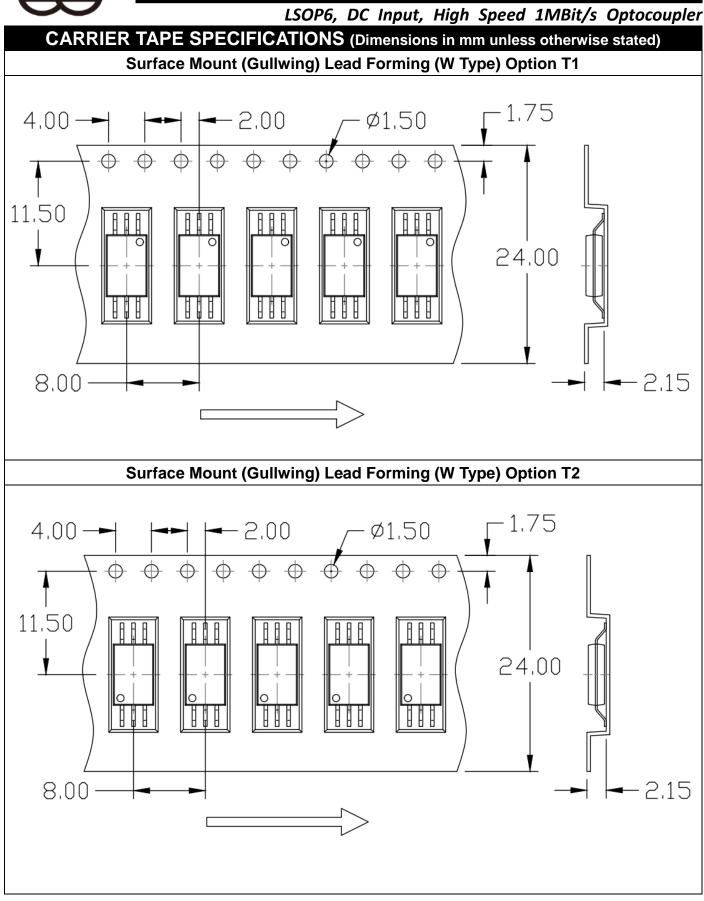




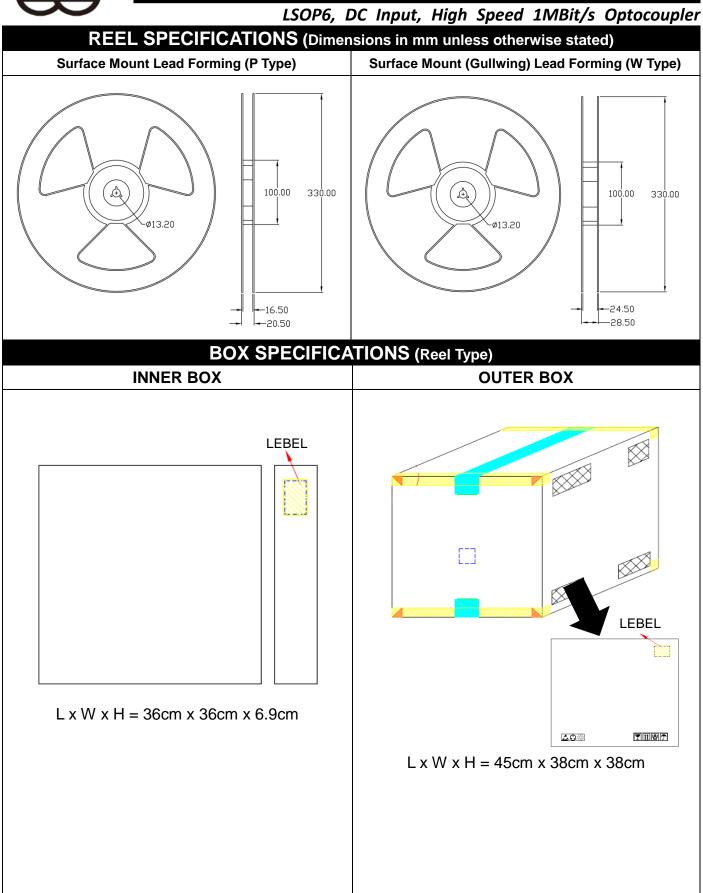














Option W T1/T2

3000 Units/Reel

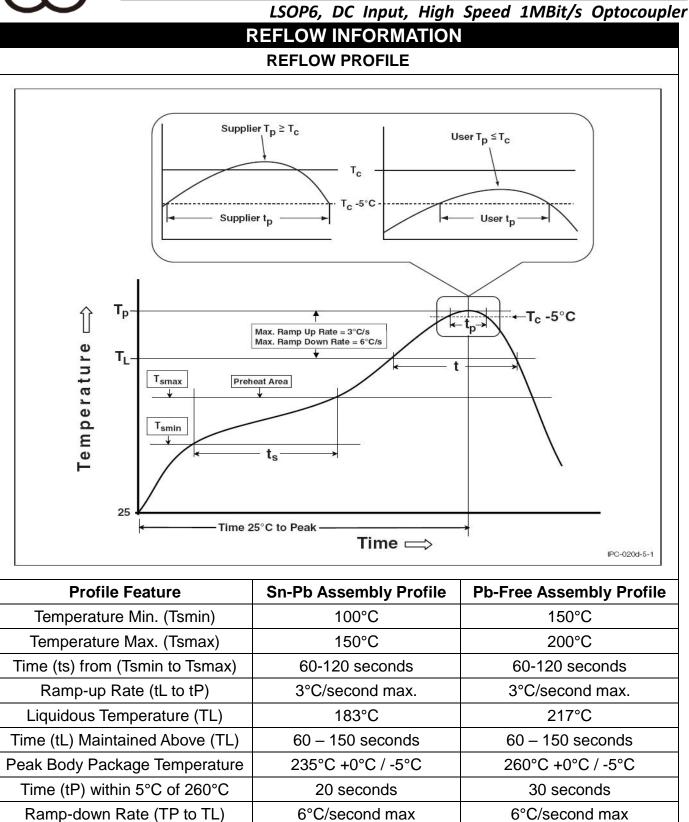
MPCS-50L Series

	LSOP6, DC Input, High Speed 1MBit/s Optocoupler							
ORDERING AND MARKING INFORMATION								
MARKING INFORMATION								
	MYYWW 50L TV		M YY WW 50L T or H V	: Company Abbr. : Year date code : 2-digit work week : Part Number H : Factory identification mark : VDE Identification(Option)				
ORD	ERING INFORMAT	ION	LABEL INFORMATION					
MPCS-50L(P/W)-ZV			結光照明光電股份有限公司 WISELITE Optronics Co., Ltd					
MPC – Company Abbr. S – Stack 50L – Part Number P/W – Lead Form Option (P-9mm Clearance or W-11mm Clearance) Z – Tape and Reel Option (T1/T2) V –VDE Option (V or None)				No : XXXXXXXXXXX e Code : XXXX r : XXXX pcs				
	PACKING QUANTITY							
Option	Option Quantity Quantity – Inner			Quantity – Outer box				
Option P T1/T2	3000 Units/Reel	3 Reels/Inner b	хох	5 Inner box/Outer box = 45k Units				

5 Inner box/Outer box = 30k Units

2 Reels/Inner box



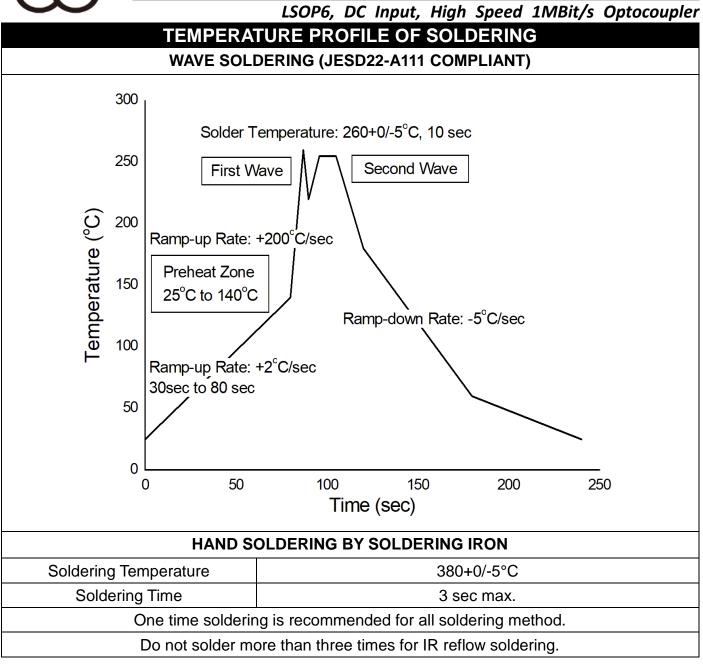


8 minutes max.

6 minutes max.

Time 25°C to Peak Temperature







LSOP6, DC Input, High Speed 1MBit/s Optocoupler DISCLAIMER

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- This product is not intended to be used for military, aircraft, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Please contact WISELITE sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify WISELITE's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.