



6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

Description

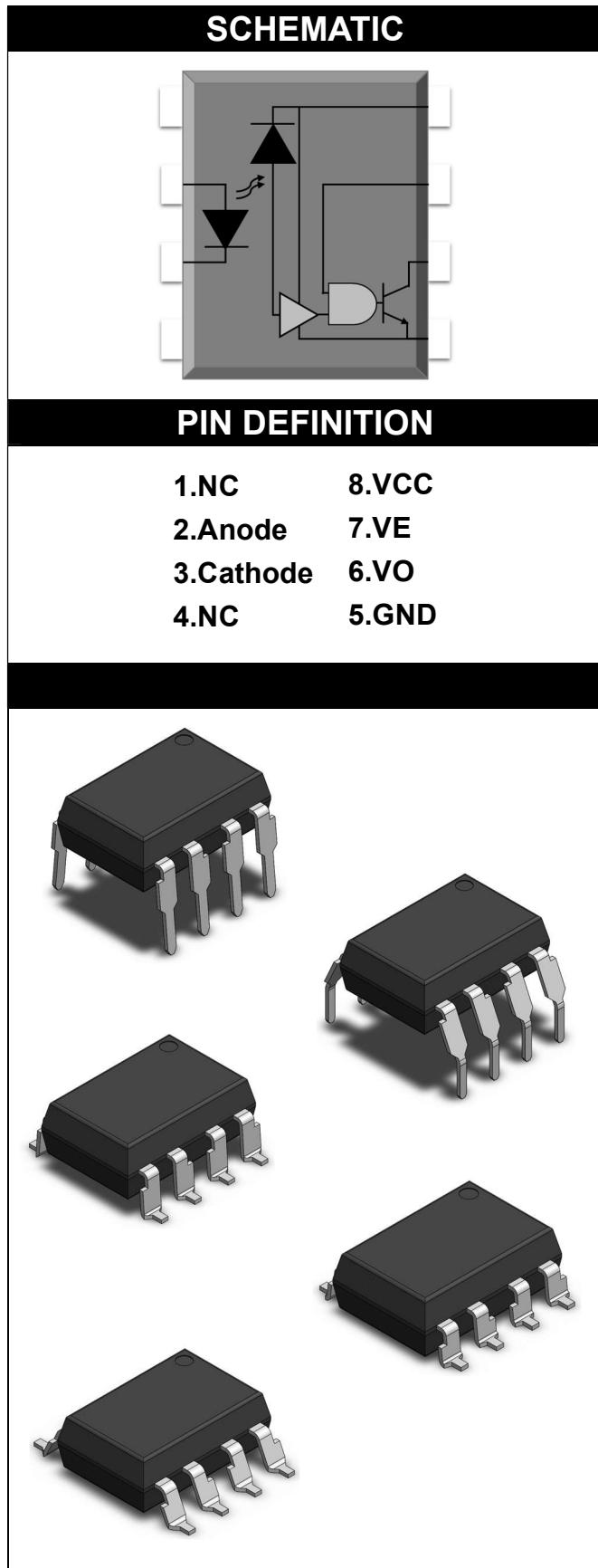
The 6N137, MPC2601, MPC2611 series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon high speed integrated photo-detector logic gate with a strobable output in a plastic DIP8 package with different lead forming options.

Features

- High isolation 5000 VRMS
- DC input with logic gate output
- Operating temperature range - 55 °C to 100 °C
- REACH compliance
- Halogen free (Optional)
- MSL class 1
- Regulatory Approvals (Pending Approved)
 - UL - UL1577
 - VDE - EN60747-5-5(VDE0884-5)
 - CQC – GB4943.1, GB8898

Applications

- Ground loop elimination
- LSTTL to TTL, LSTTL or CMOS
- Line receiver, data transmission
- Data multiplexing
- Switching power supply
- Pulse transformer replacement
- Computer-peripheral interface





6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	VALUE	UNIT	Note
INPUT				
Forward Current	I _F	25	mA	
Peak Forward Current	I _{FP}	50	mA	1
Peak Transient Current	I _{F(trans)}	1	A	2
Reverse Voltage	V _R	5	V	
Enable Voltage	V _E	VCC+0.5	V	
Input Power Dissipation	P _I	100	mW	
OUTPUT				
Supply Voltage	V _{CC}	7	V	
Output Voltage	V _O	7	V	
Output Current	I _O	50	mA	
Output Power Dissipation	P _O	85	mW	
COMMON				
Total Power Dissipation	P _{tot}	200	mW	
Isolation Voltage	V _{iso}	5000	V _{rms}	3
Operating Temperature	T _{opr}	-55~100	°C	
Storage Temperature	T _{stg}	-55~125	°C	
Soldering Temperature	T _{sol}	260	°C	4

Note 1. 50% duty, 1ms P.W

Note 2. ≤1μs P.W, 300pps

Note 3. AC For 1 Minute, R.H. = 40 ~ 60%

Note 4. For 10 seconds



6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

RECOMMENDED OPERATION CONDITIONS					
PARAMETER		SYMBOL		MIN.	MAX.
Operating Temperature		TA		-40	100
Supply Voltage		VCC		2.7	3.6
		VCC		4.5	5.5
Low Level Input Current		IFL		0	250
High Level Input Current		IFH		5	15
Low Level Enable Voltage		VEL		0	0.8
High Level Enable Voltage		VEH		2	VCC
Output Pull-up Resistor		RL		330	4k
Fan Out (at RL=1kΩ per channel)		N		-	5
TTL Loads					

ELECTRICAL OPTICAL CHARACTERISTICS at Ta=25°C						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
INPUT						
Forward Voltage	V _F	-	1.38	1.8	V	I _F =10mA
Reverse Current	I _R	-	-	10	µA	V _R =5V
Input Capacitance	C _{in}	-	13	-	pF	V=0, f=1MHz
OUTPUT						
High Level Supply Current	I _{CCH}	-	6.3	10	mA	I _F =0mA, V _E =0.5V, V _{CC} =5.5V
Low Level Supply Current	I _{CCL}	-	8.3	13	mA	I _F =10mA, V _{CC} =5.5V
High Level Enable Current	I _{EH}	-	-0.52	-1.6	mA	V _E =2.0V, V _{CC} =5.5V
Low Level Enable Current	I _{EL}	-	-0.75	-1.6	mA	V _E =0.5V, V _{CC} =5.5V
High Level Enable Voltage	V _{EH}	2.0	-	-	V	I _F =10mA, V _{CC} =5.5V
Low Level Enable Voltage	V _{EL}	-	-	0.8	V	I _F =10mA, V _{CC} =5.5V
TRANSFER CHARACTERISTICS (Ta=-40 to 85°C)						
High Level Output Current	I _{OH}	-	0.73	100	µA	V _{CC} =5.5V, V _O =5.5V, I _F =250µA, V _E =2.0V
Low Level Output Voltage	V _{OL}	-	0.28	0.6	V	V _{CC} =5.5V, I _F =5mA, V _E =2.0V, I _{CL} =13mA
Input Threshold Current	I _{FT}	-	2.5	5	mA	V _{CC} =5.5V, V _O =0.6V, V _E =2.0V, I _{OL} =13mA
Isolation Resistance	R _{iso}	10 ¹²	10 ¹⁴	-	Ω	DC500V, 40 ~ 60% R.H.
Floating Capacitance	C _{IO}	-	1.0	-	pF	V=0, f=1MHz



6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

ELECTRICAL OPTICAL CHARACTERISTICS							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS (Ta=-40 to 85°C, V _{CC} =5V, I _F =7.5mA unless specified otherwise)							
Propagation Delay Time to Output Low Level	TPHL	-	35	75	ns	C _L =15pF, R _L =350Ω, Ta=25°C	
Propagation Delay Time to Output High Level	TPLH	-	40	75	ns	C _L =15pF, R _L =350Ω, Ta=25°C	
Pulse Width Distortion	TPHL-TPLH	-	5	35	ns	C _L =15pF, R _L =350Ω	
Rise Time	tr	-	27	-	ns	C _L =15pF, R _L =350Ω	
Fall Time	tf	-	7	-	ns	C _L =15pF, R _L =350Ω	
Enable Propagation Delay Time to Output Low Level	TEHL	-	15	-	ns	I _F =7.5mA, V _{EH} =3.5V, C _L =15pF, R _L =350Ω	
Enable Propagation Delay Time to Output High Level	TELH	-	15	-	ns	I _F =7.5mA, V _{EH} =3.5V, C _L =15pF, R _L =350Ω	
Common Mode Transient Immunity at Logic High	6N137	CMH	-	-	-	V/μs	I _F = 7.5mA , V _{OH} =2.0V, R _L =350Ω, Ta=25°C V _{CM} =10Vp-p
	MPC2601		5000	-	-		I _F = 7.5mA , V _{OH} =2.0V, R _L =350Ω, Ta=25°C V _{CM} =50Vp-p
	MPC2611		10000	-	-		I _F = 7.5mA , V _{OH} =2.0V, R _L =350Ω, Ta=25°C V _{CM} =400Vp-p
Common Mode Transient Immunity at Logic Low	6N137	CML	-	-	-	V/μs	I _F = 0mA , V _{OH} =0.8V, R _L =350Ω, Ta=25°C V _{CM} =10Vp-p
	MPC2601		5000	-	-		I _F = 0mA , V _{OH} =0.8V, R _L =350Ω, Ta=25°C V _{CM} =50Vp-p
	MPC2611		10000	-	-		I _F = 0mA , V _{OH} =0.8V, R _L =350Ω, Ta=25°C V _{CM} =400Vp-p



6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

ELECTRICAL OPTICAL CHARACTERISTICS at Ta=25°C							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
INPUT							
Forward Voltage	V _F	-	1.38	1.8	V	I _F =10mA	
Reverse Current	I _R	-	-	10	µA	V _R =5V	
Input Capacitance	C _{in}	-	13	-	pF	V=0, f=1MHz	
OUTPUT							
High Level Supply Current	I _{CCH}	-	4.3	10	mA	I _F =0mA, V _E =0.5V, V _{CC} =3.3V	
Low Level Supply Current	I _{CCL}	-	6.4	13	mA	I _F =10mA, V _{CC} =3.3V	
High Level Enable Current	I _{EH}	-	-0.21	-1.6	mA	V _E =2.0V, V _{CC} =3.3V	
Low Level Enable Current	I _{EL}	-	-0.42	-1.6	mA	V _E =0.5V, V _{CC} =3.3V	
High Level Enable Voltage	V _{EH}	2.0	-	-	V	I _F =10mA, V _{CC} =3.3V	
Low Level Enable Voltage	V _{EL}	-	-	0.8	V	I _F =10mA, V _{CC} =3.3V	
TRANSFER CHARACTERISTICS (Ta=-40 to 85°C)							
High Level Output Current	I _{OH}	-	4.1	100	µA	V _{CC} =3.3V, V _O =3.3V, I _F =250µA, V _E =2.0V	
Low Level Output Voltage	V _{OL}	-	0.29	0.6	V	V _{CC} =3.3V, I _F =5mA, V _E =2.0V, I _{CL} =13mA	
Input Threshold Current	I _{FT}	-	2.2	5	mA	V _{CC} =3.3V, V _O =0.6V, V _E =2.0V, I _{OL} =13mA	
Isolation Resistance	R _{iso}	10 ¹²	10 ¹⁴	-	Ω	DC500V, 40 ~ 60% R.H.	
Floating Capacitance	C _{IO}	-	1.0	-	pF	V=0, f=1MHz	



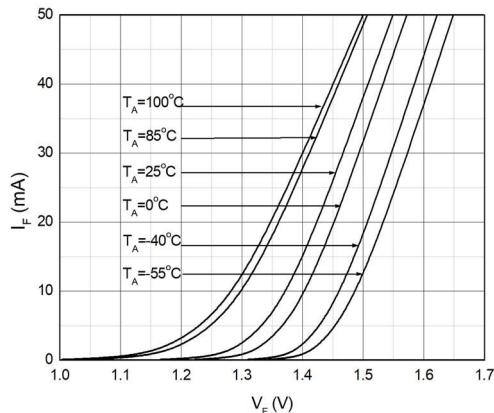
6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

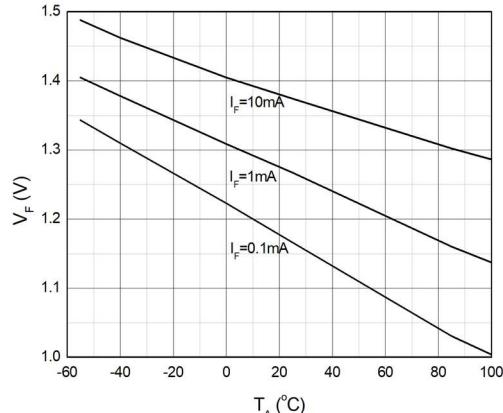
ELECTRICAL OPTICAL CHARACTERISTICS							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS (Ta=-40 to 85°C, V _{CC} =3.3V, I _F =7.5mA unless specified otherwise)							
Propagation Delay Time to Output Low Level	TPHL	-	35	75	ns	C _L =15pF, R _L =350Ω, Ta=25°C	
Propagation Delay Time to Output High Level	TPLH	-	47	75	ns	C _L =15pF, R _L =350Ω, Ta=25°C	
Pulse Width Distortion	TPHL-TPLH	-	12	35	ns	C _L =15pF, R _L =350Ω	
Rise Time	tr	-	30	-	ns	C _L =15pF, R _L =350Ω	
Fall Time	tf	-	8.5	-	ns	C _L =15pF, R _L =350Ω	
Enable Propagation Delay Time to Output Low Level	TEHL	-	15	-	ns	I _F =7.5mA, V _{EH} =3.3.3V, C _L =15pF, R _L =350Ω	
Enable Propagation Delay Time to Output High Level	TELH	-	15	-	ns	I _F =7.5mA, V _{EH} =3.3.3V, C _L =15pF, R _L =350Ω	
Common Mode Transient Immunity at Logic High	6N137	CMH	-	-	-	V/μs	I _F = 7.5mA , V _{OH} =2.0V, R _L =350Ω, Ta=25°C V _{CM} =10Vp-p
	MPC2601		5000	-	-		I _F = 7.5mA , V _{OH} =2.0V, R _L =350Ω, Ta=25°C V _{CM} =50Vp-p
	MPC2611		10000	-	-		I _F = 7.5mA , V _{OH} =2.0V, R _L =350Ω, Ta=25°C V _{CM} =400Vp-p
Common Mode Transient Immunity at Logic Low	6N137	CML	-	-	-	V/μs	I _F = 0mA , V _{OH} =0.8V, R _L =350Ω, Ta=25°C V _{CM} =10Vp-p
	MPC2601		5000	-	-		I _F = 0mA , V _{OH} =0.8V, R _L =350Ω, Ta=25°C V _{CM} =50Vp-p
	MPC2611		10000	-	-		I _F = 0mA , V _{OH} =0.8V, R _L =350Ω, Ta=25°C V _{CM} =400Vp-p

CHARACTERISTIC CURVES

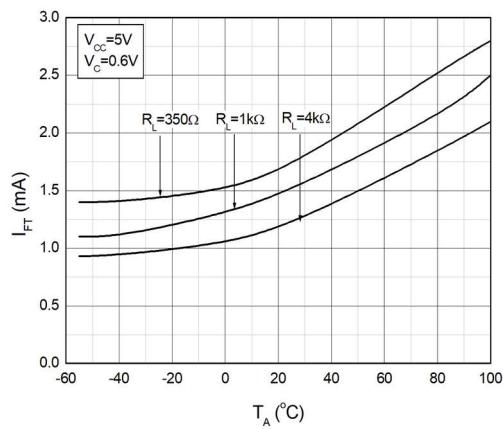
**Fig.1 Forward Current
vs. Forward Voltage**



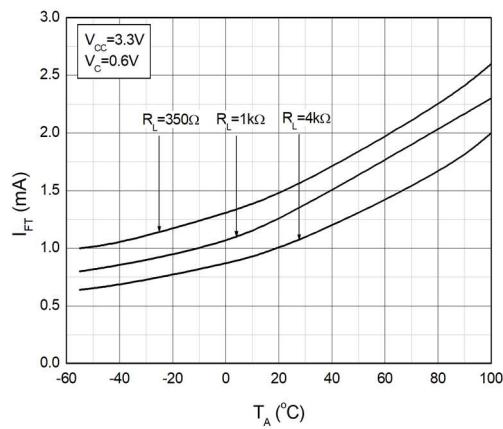
**Fig.2 Forward Voltage
vs. Ambient Temperature**



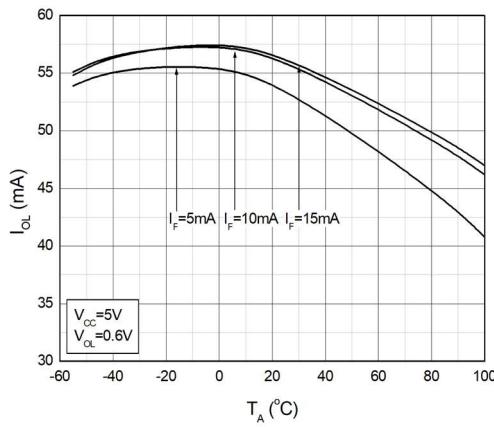
**Fig.3 Input Threshold Current
vs. Ambient Temperature**



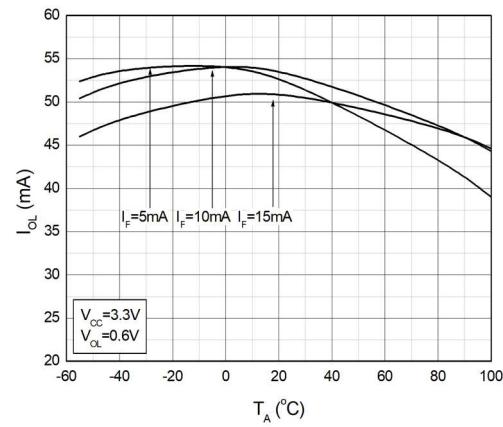
**Fig.4 Input Threshold Current
vs. Ambient Temperature**



**Fig.5 Low Level Output Current
vs. Ambient Temperature**



**Fig.6 Low Level Output Current
vs. Ambient Temperature**





6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

CHARACTERISTIC CURVES

Fig.7 Low Level Output Voltage
vs. Ambient Temperature

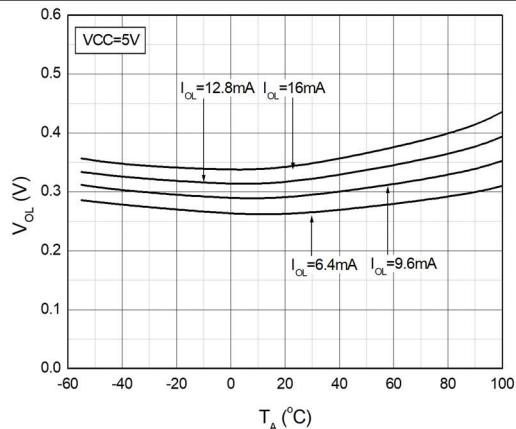


Fig.8 Low Level Output Voltage
vs. Ambient Temperature

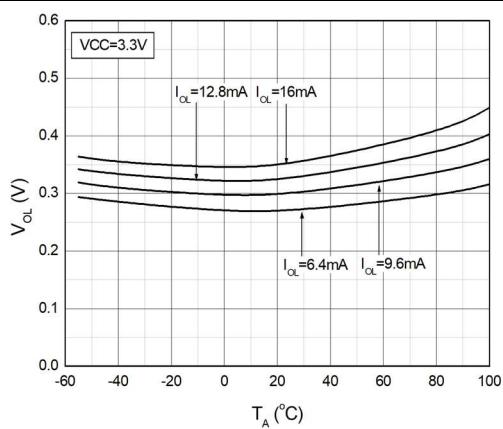


Fig.9 High Level Output Current
vs. Ambient Temperature

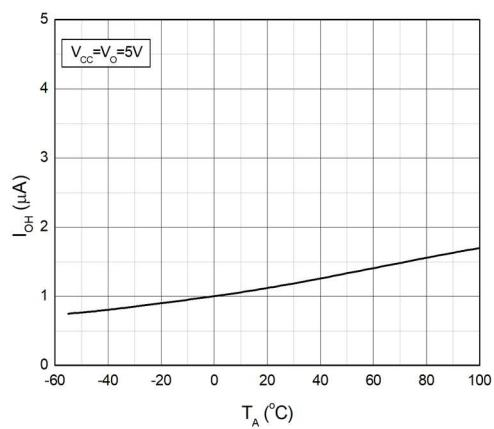


Fig.10 High Level Output Current
vs. Ambient Temperature

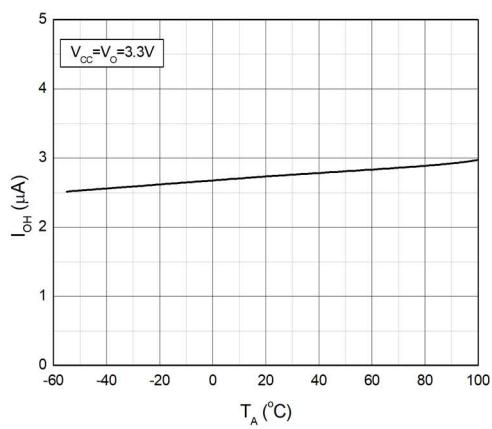


Fig.11 Output Voltage
vs. Forward Current

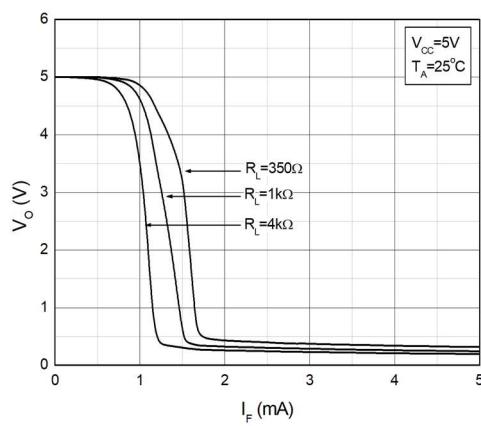
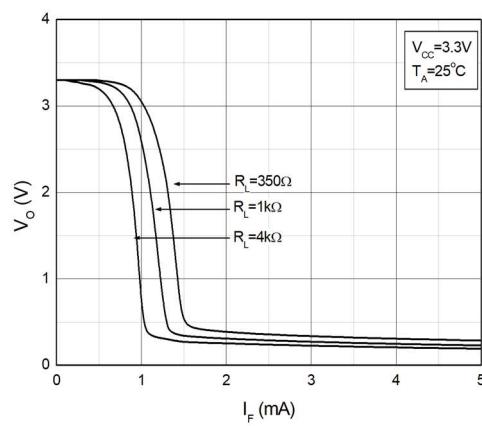


Fig.12 Output Voltage
vs. Forward Current





6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

CHARACTERISTIC CURVES

Fig.13 Propagation Delay
vs. Forward Current

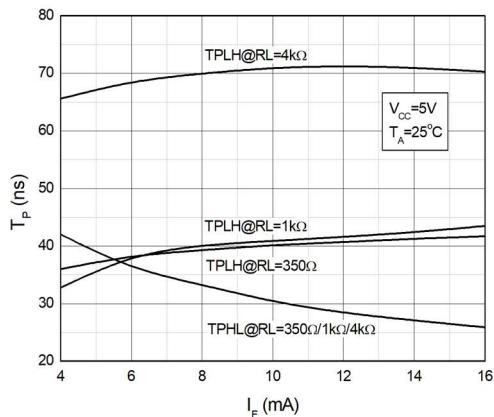


Fig.14 Propagation Delay
vs. Forward Current

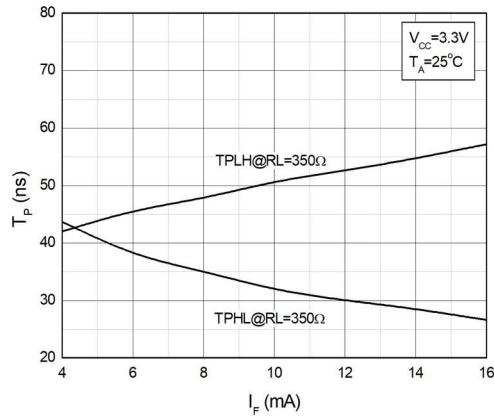


Fig.15 Rise and Fall Time
vs. Ambient Temperature

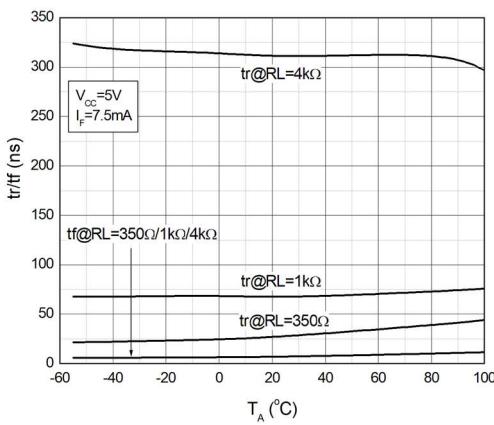


Fig.16 Rise and Fall Time
vs. Ambient Temperature

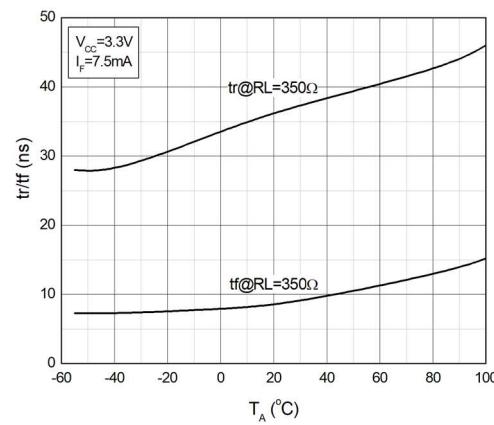


Fig.17 Propagation Delay
vs. Ambient Temperature

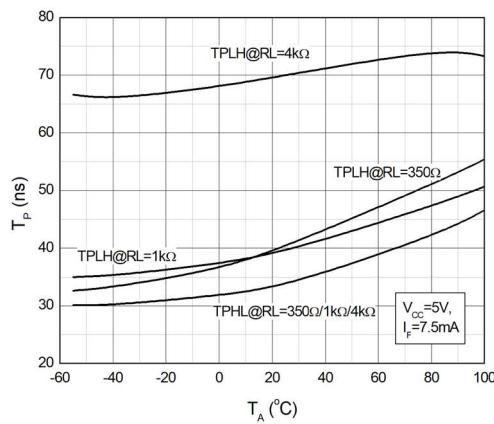
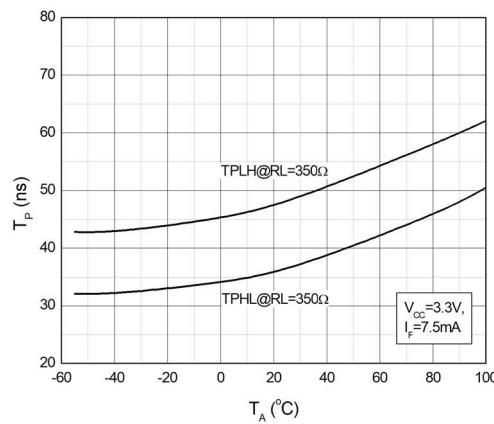


Fig.18 Propagation Delay
vs. Ambient Temperature





6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

CHARACTERISTIC CURVES

Fig.19 Pulse Width Distortion
vs. Ambient Temperature

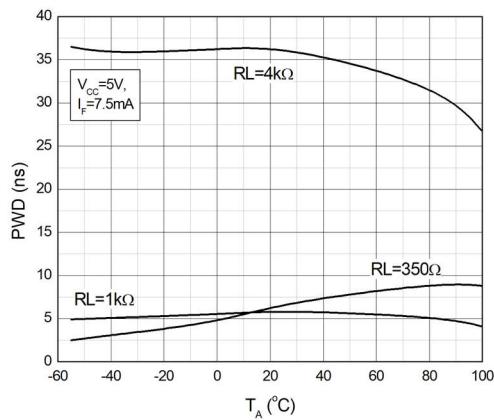


Fig.20 Pulse Width Distortion
vs. Ambient Temperature

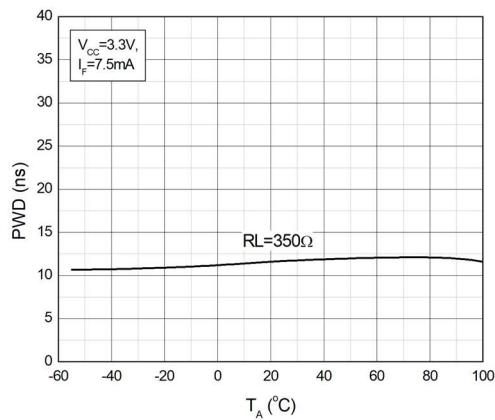


Fig.21 Enable Propagation Delay
vs. Ambient Temperature

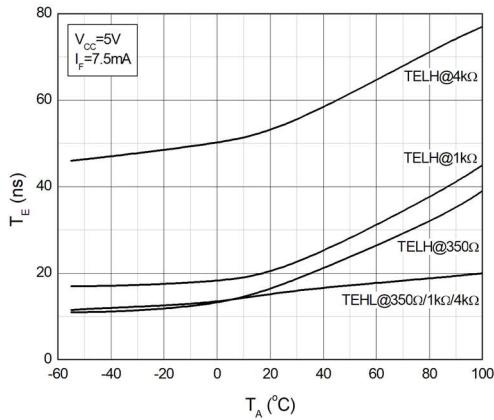
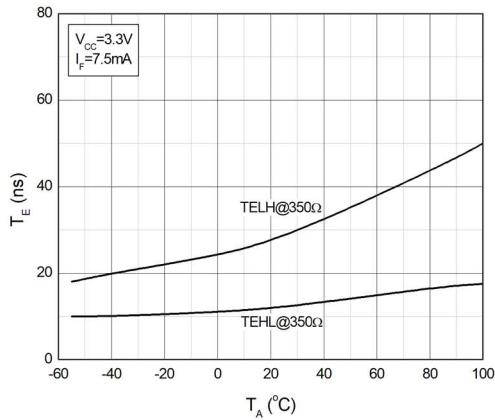


Fig.22 Enable Propagation Delay
vs. Ambient Temperature





6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

TEST CIRCUITS

Fig.23 Test Circuits for TPHL, TPLH, tr, tf

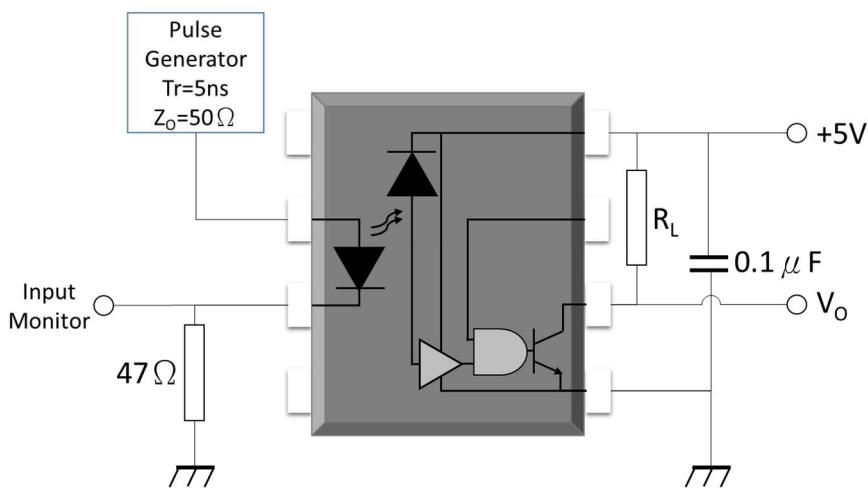
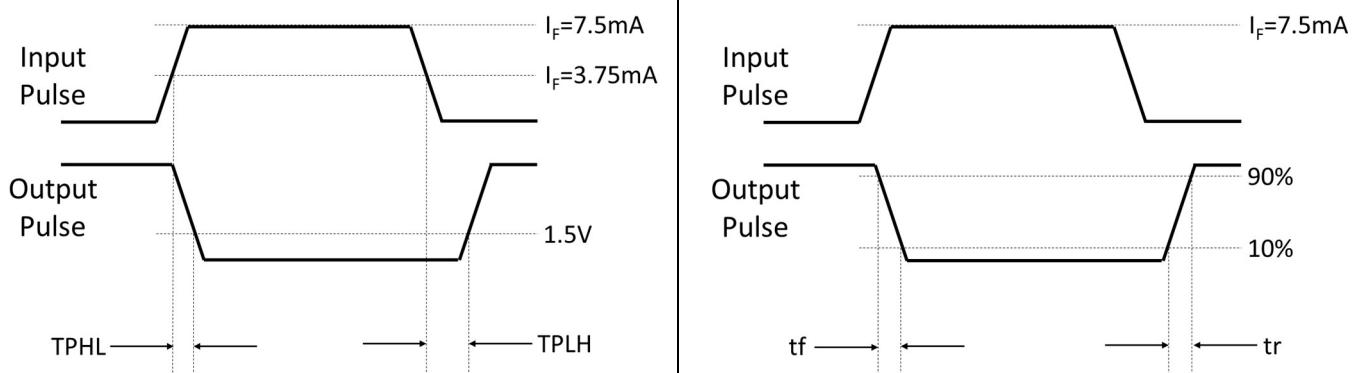


Fig.24 Waveforms of TPHL, TPLH, tr, tf





6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

TEST CIRCUITS

Fig.25 Test Circuits for TEHL, TELH

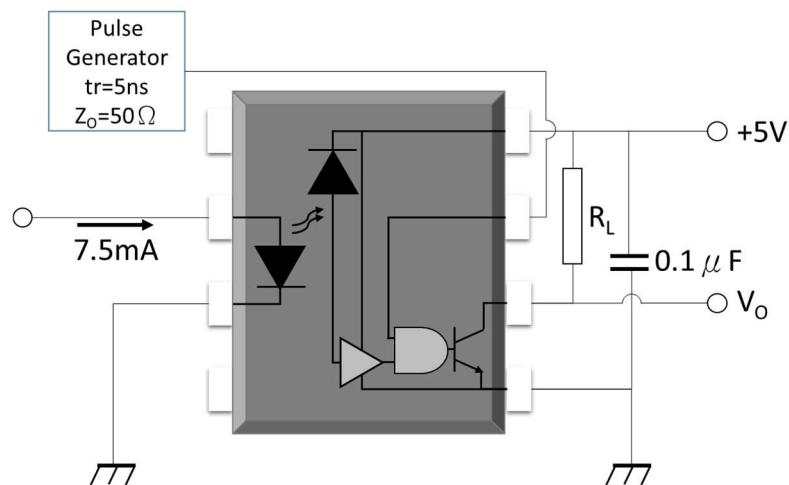
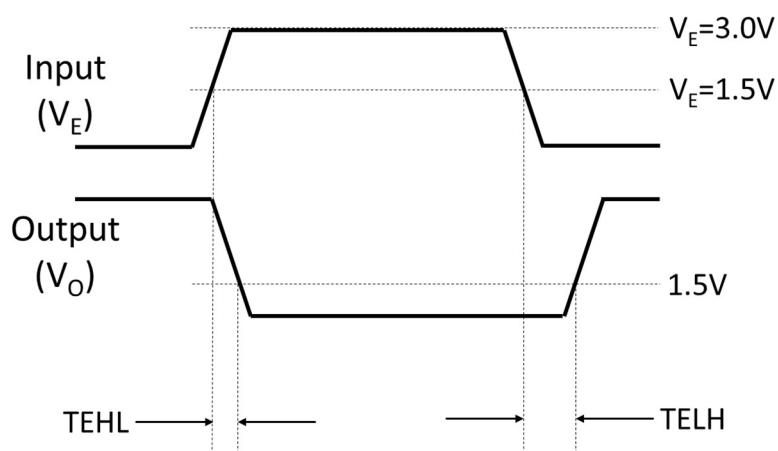


Fig.26 Waveforms of TEHL, TELH



TEST CIRCUITS

Fig.25 Test Circuits for Common Mode Transient Immunity

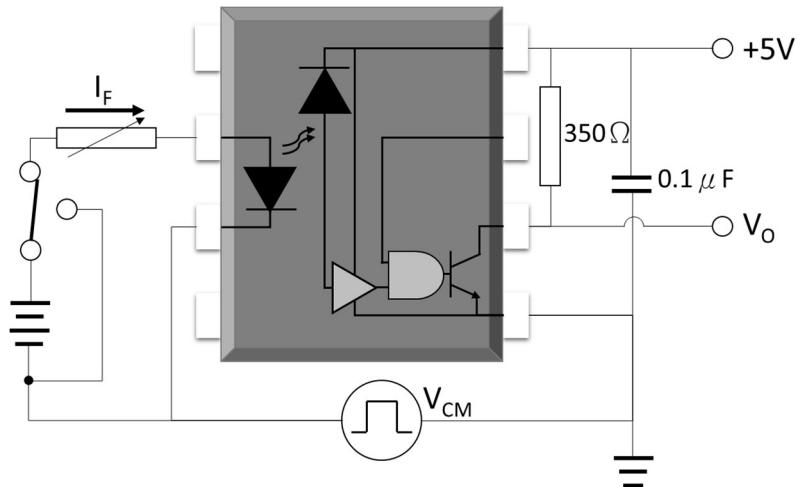
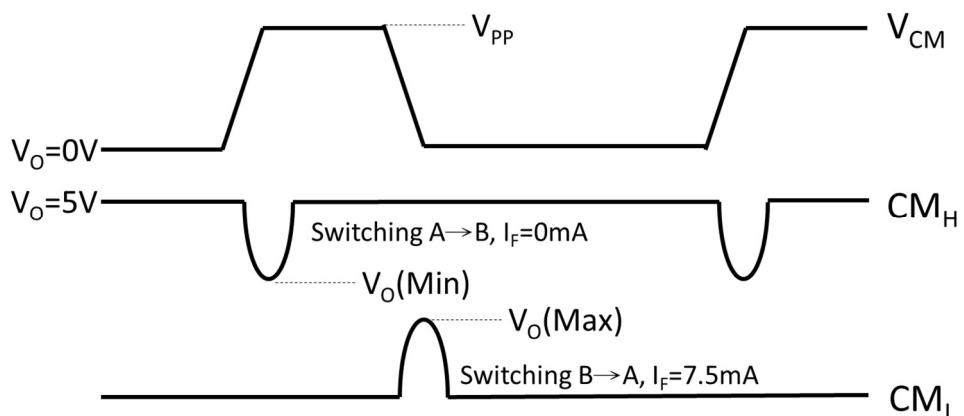


Fig.26 Waveforms of Common Mode Transient Immunity



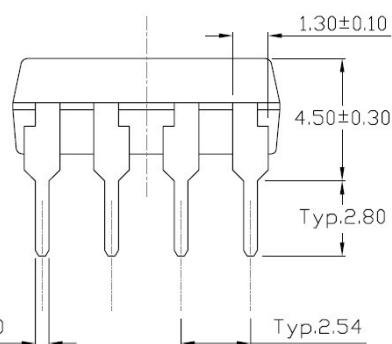
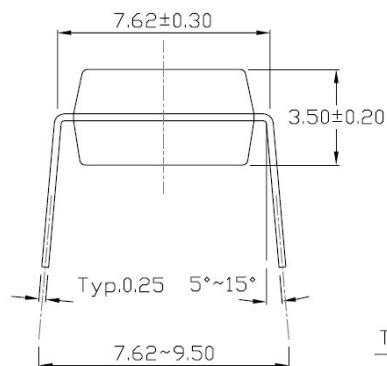
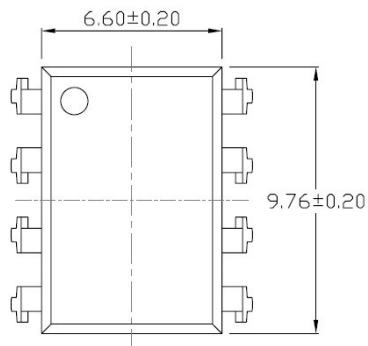


6N137, MPC2601, MPC2611 Series

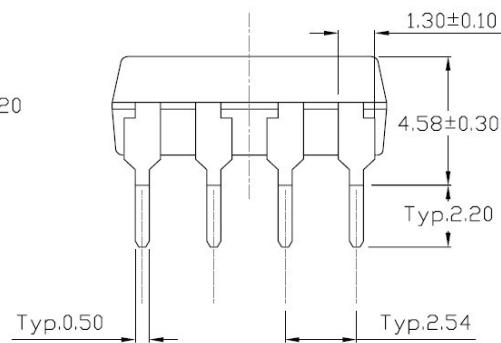
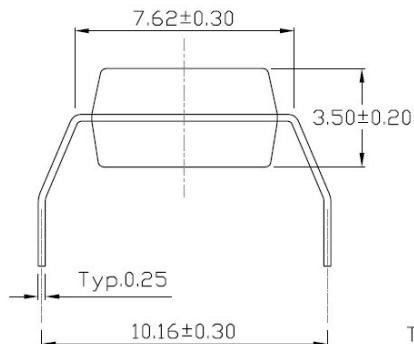
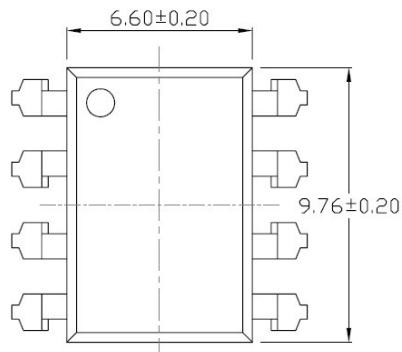
DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)

Standard DIP – Through Hole (DIP Type)



Gullwing (400mil) Lead Forming – Through Hole (M Type)



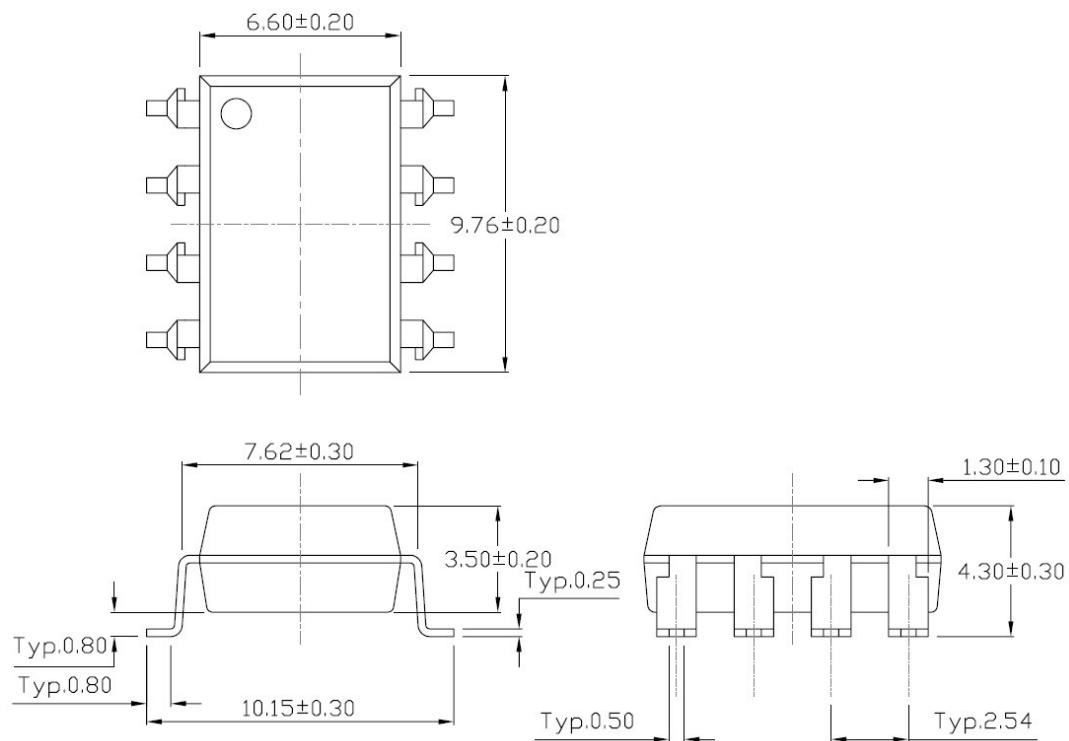


6N137, MPC2601, MPC2611 Series

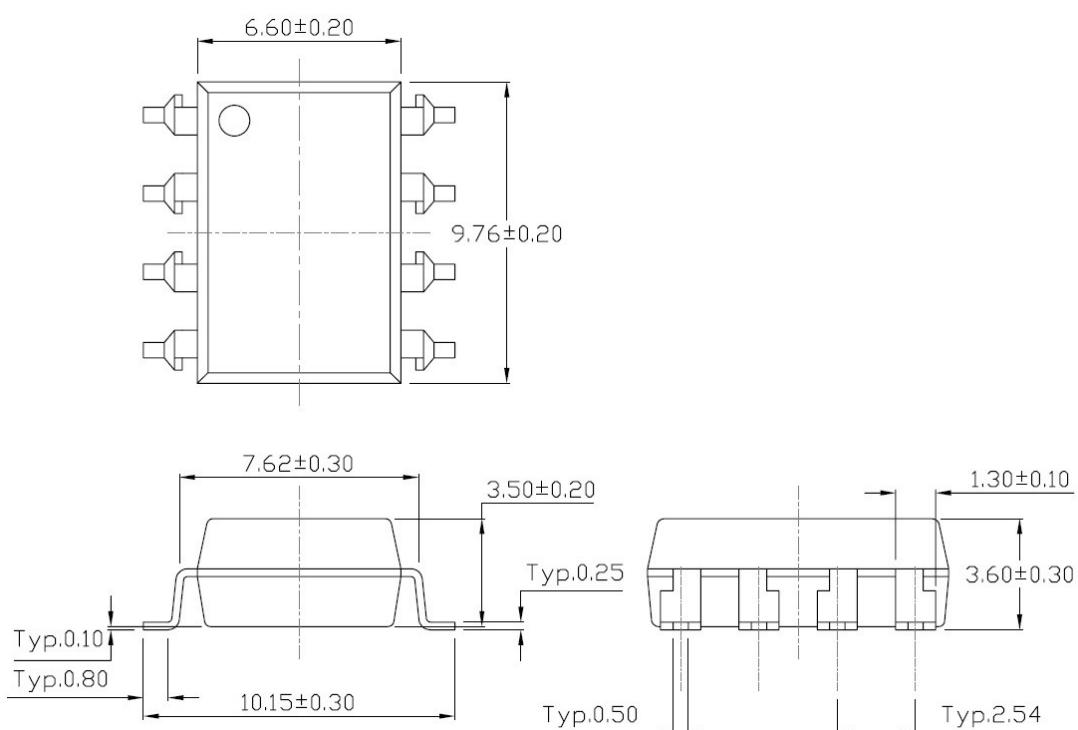
DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)

Surface Mount Lead Forming (S Type)



Surface Mount (Low Profile) Lead Forming (SL Type)



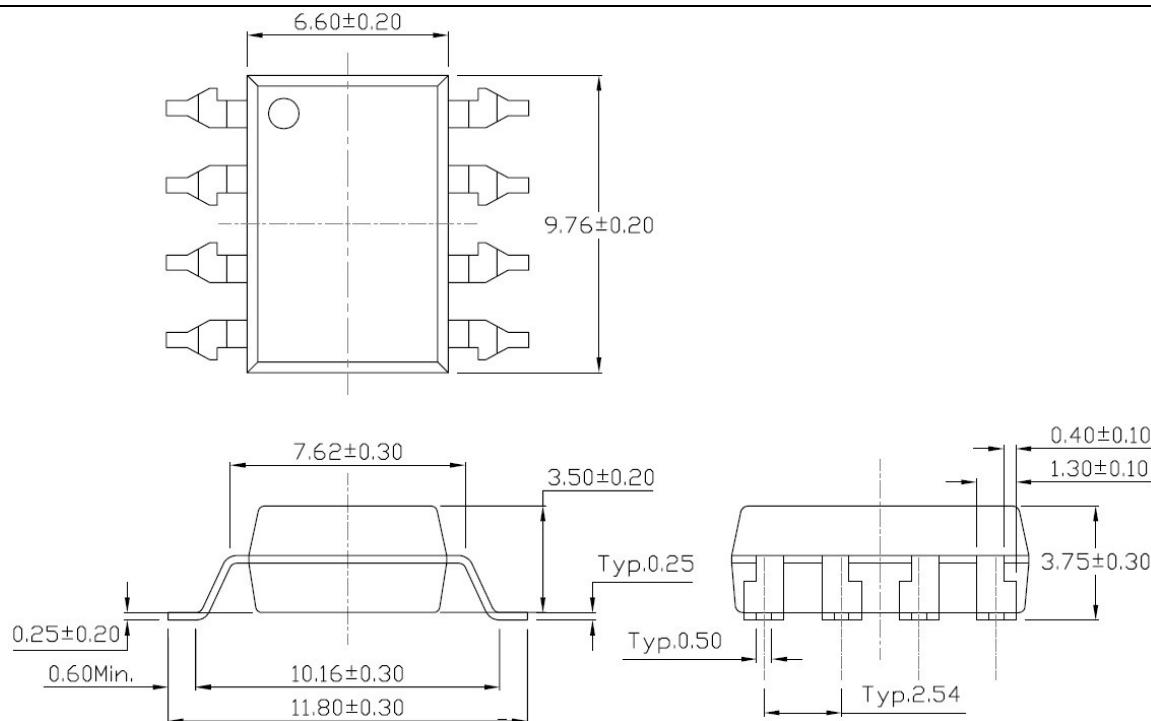


6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

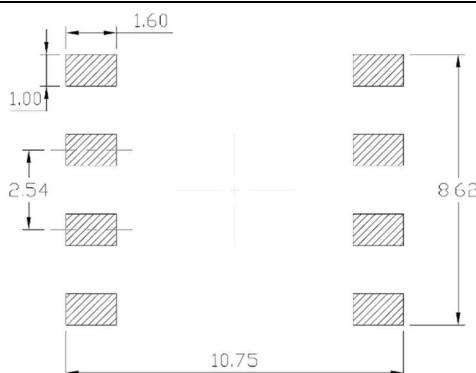
PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)

Surface Mount (Gullwing) Lead Forming (SLM Type)

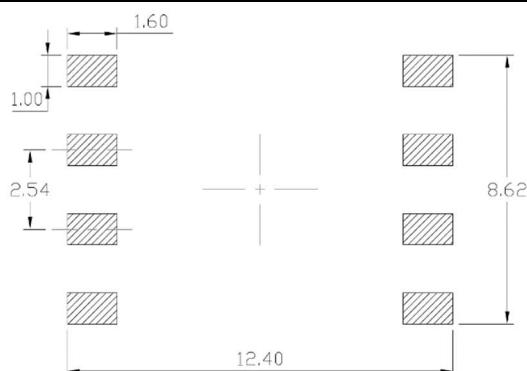


Recommended Solder Mask (Dimensions in mm unless otherwise stated)

Surface Mount Lead Forming & Surface Mount (Low Profile) Lead Forming



Surface Mount (Gullwing) Lead Forming



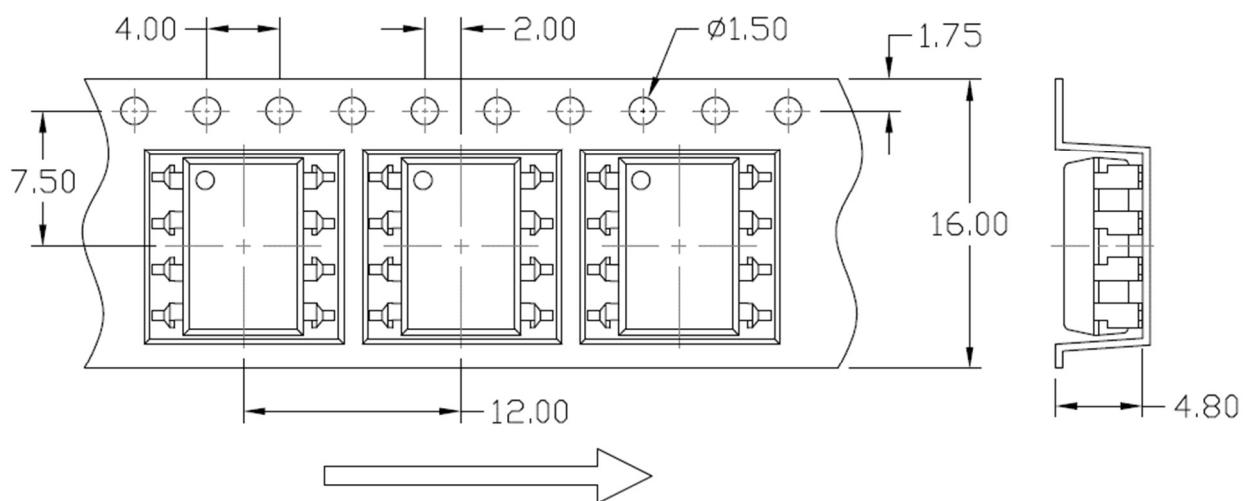


6N137, MPC2601, MPC2611 Series

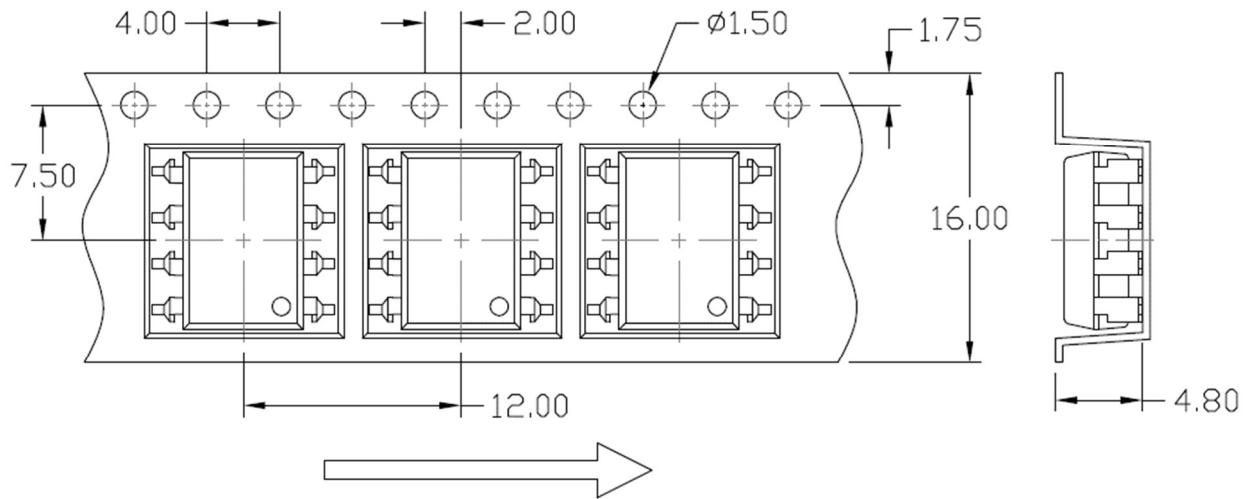
DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

Carrier Tape Specifications (Dimensions in mm unless otherwise stated)

Option S(T1) & SL(T1)



Option S(T2) & SL(T2)



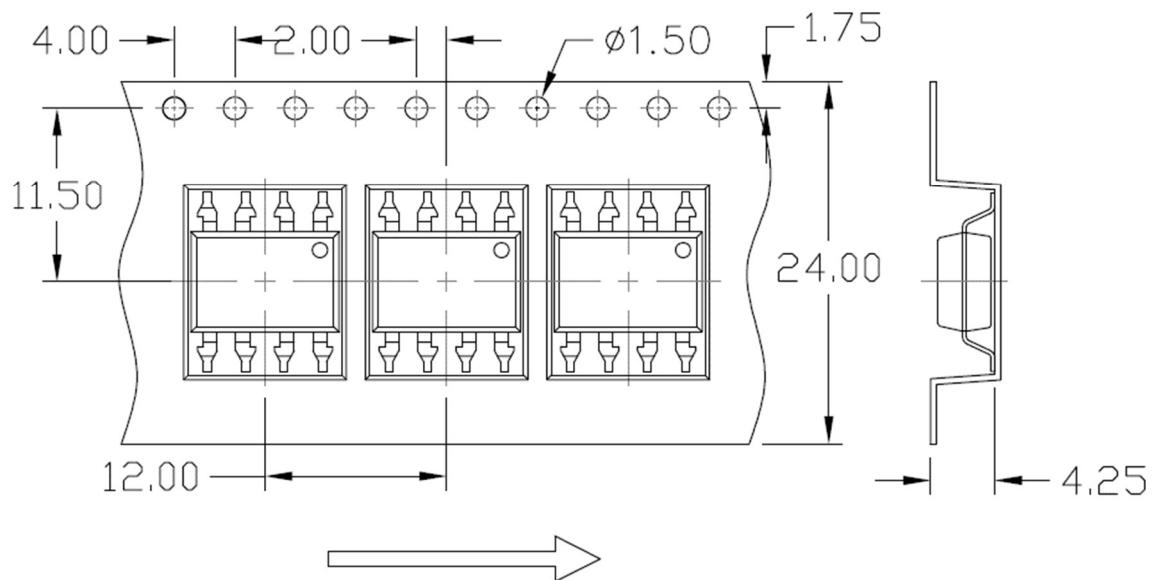


6N137, MPC2601, MPC2611 Series

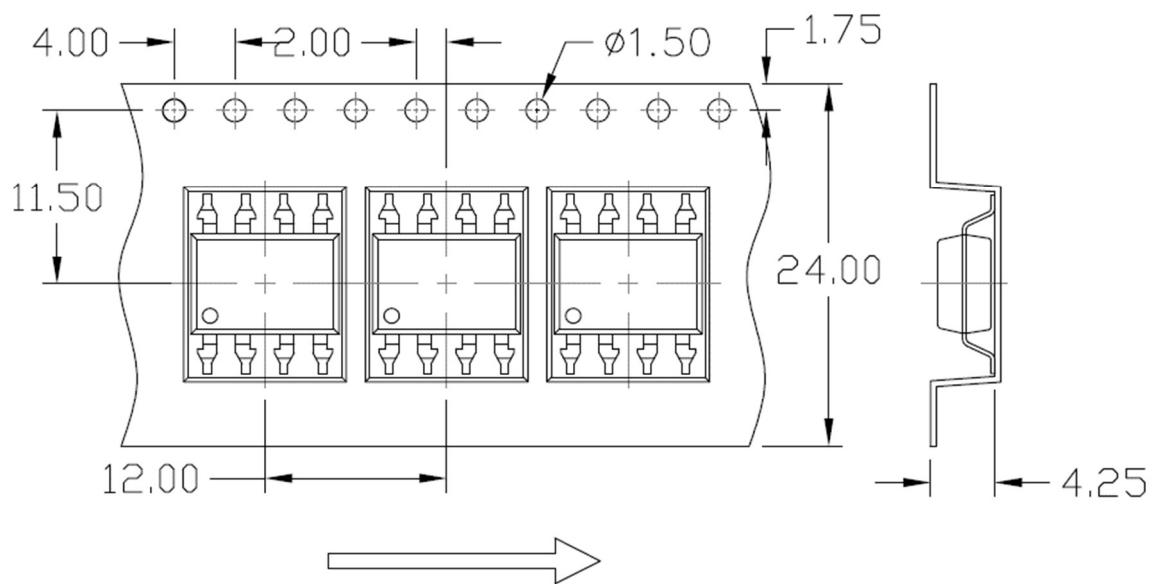
DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

Carrier Tape Specifications (Dimensions in mm unless otherwise stated)

Option SLM(T1)



Option SLM(T2)



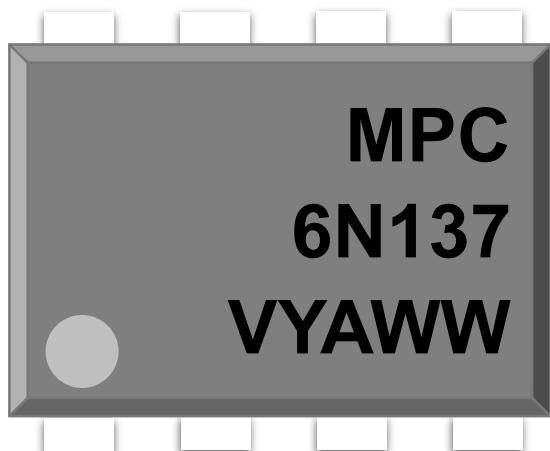


6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

ORDERING AND MARKING INFORMATION

MARKING INFORMATION



MPC : Company Abbr.
6N137 : Part Number
V : VDE Option
Y : Fiscal Year
A : Manufacturing Code
WW : Work Week

ORDERING INFORMATION

6N137(Y)(Z)-GV

6N137 – Part Number

Y – Lead Form Option (M/S/SL/SLM/None)

Z – Tape and Reel Option (T1/T2)

G – Material Option (G: Green, None: Non-Green)

V – VDE Option (V or None)

PACKING QUANTITY

Option	Description	Quantity
None	Standard 8 Pin Dip	50Units/Tube
M	Gullwing(400mil) Lead Forming	50Units/Tube
S(T1)	Surface Mount Lead Forming – With Option 1 Taping	1000 Units/Reel
S(T2)	Surface Mount Lead Forming – With Option 2 Taping	1000 Units/Reel
SL(T1)	Surface Mount Lead Forming(Low Profile) – With Option 1 Taping	1000 Units/Reel
SL(T2)	Surface Mount Lead Forming(Low Profile) – With Option 2 Taping	1000 Units/Reel

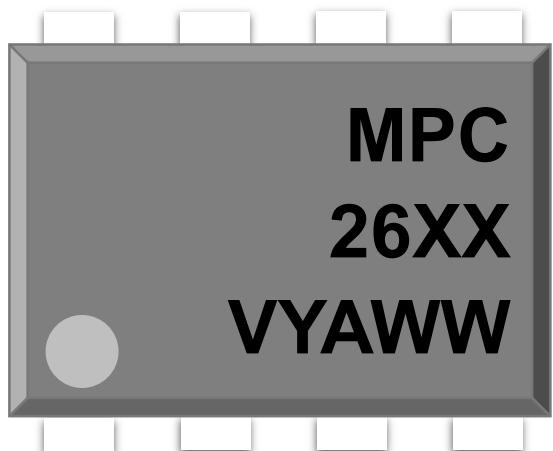


6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

ORDERING AND MARKING INFORMATION

MARKING INFORMATION



MPC : Company Abbr.
26XX : Part Number & Rank
V : VDE Option
Y : Fiscal Year
A : Manufacturing Code
WW : Work Week

ORDERING INFORMATION

MPC26XX(Y)(Z)-GV

MPC – Company Abbr.

26XX – Rank (01/11)

Y – Lead Form Option (M/S/SL/SLM/None)

Z – Tape and Reel Option (T1/T2)

G – Material Option (G: Green, None: Non-Green)

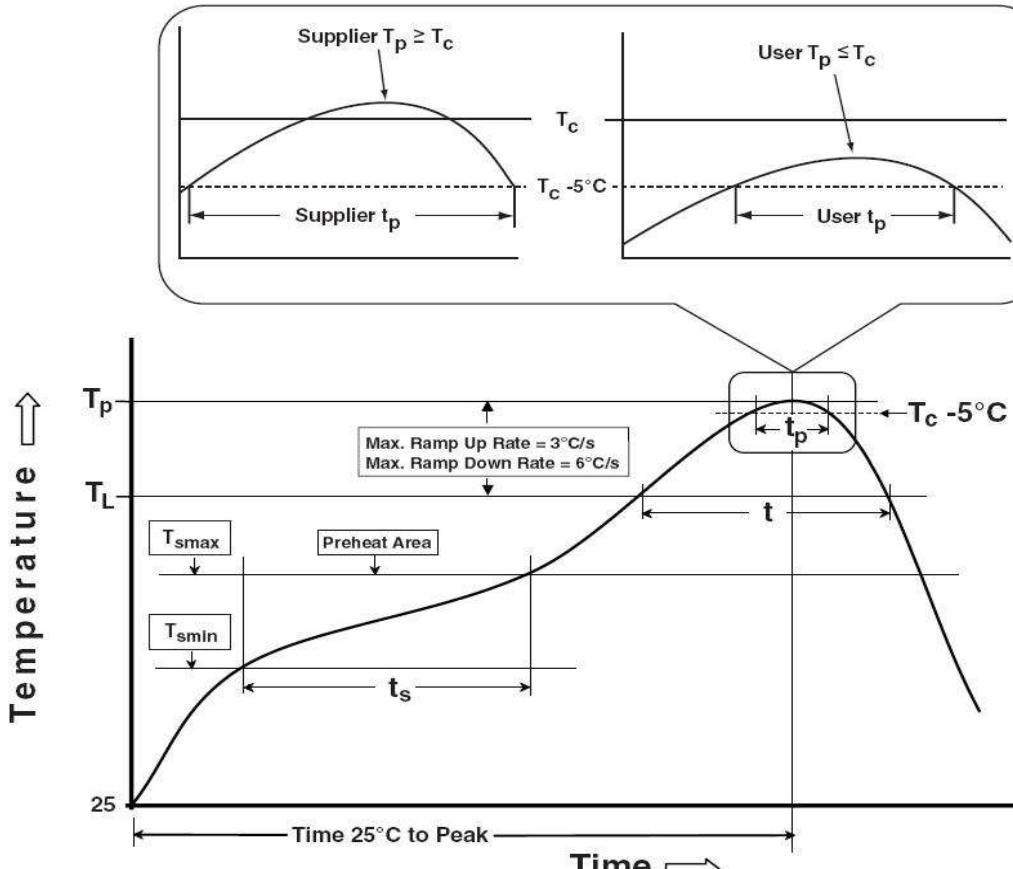
V – VDE Option (V or None)

PACKING QUANTITY

Option	Description	Quantity
None	Standard 8 Pin Dip	50Units/Tube
M	Gullwing(400mil) Lead Forming	50Units/Tube
S(T1)	Surface Mount Lead Forming – With Option 1 Taping	1000 Units/Reel
S(T2)	Surface Mount Lead Forming – With Option 2 Taping	1000 Units/Reel
SL(T1)	Surface Mount Lead Forming(Low Profile) – With Option 1 Taping	1000 Units/Reel
SL(T2)	Surface Mount Lead Forming(Low Profile) – With Option 2 Taping	1000 Units/Reel

REFLOW INFORMATION

REFLOW PROFILE


IPC-020d-5-1

Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (T_{smin})	100	150°C
Temperature Max. (T_{smax})	150	200°C
Time (t_s) from (T_{smin} to T_{smax})	60-120 seconds	60-120 seconds
Ramp-up Rate (t_L to t_P)	3°C/second max.	3°C/second max.
Liquidous Temperature (T_L)	183°C	217°C
Time (t_L) Maintained Above (T_L)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (t_P) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (T_P to T_L)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.



6N137, MPC2601, MPC2611 Series

DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

DISCLAIMER

- MEMCHIP is continually improving the quality, reliability, function and design. MEMCHIP reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
- MEMCHIP makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, MEMCHIP disclaims (a) any and all liability arising out of the application or use of any product, (b) any and all liability, including without limitation special, consequential or incidental damages, and (c) any and all implied warranties, including warranties of fitness for particular
- The products shown in this publication are designed for the general use in electronic applications such as office automation, equipment, communications devices, audio/visual equipment, electrical application and instrumentation purpose, non-infringement and merchantability.
- This product is not intended to be used for military, aircraft, automotive, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Please contact MEMCHIP sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify MEMCHIP's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.



6N137, MPC2601, MPC2611 Series
DIP8, 10Mbit/s High Speed Logic Gate Photo Coupler

版本 Rev.	生效日期 Effective Date	作者 Applicant	内容 Change Description
0.2	—	—	—