

Description

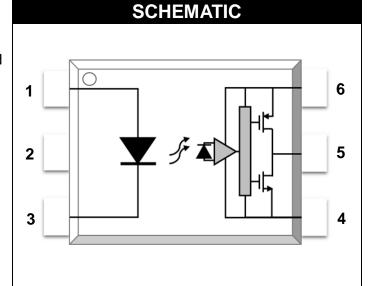
The MPCS-5701 series Photocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control inverter applications and inverters in power supply system. It contains an LED optically coupled to an integrated circuit with a power output stage. The Photocoupler operational parameters are guaranteed over the temperature range from -40°C ~ +110°C.

Features

- 0.8 A maximum peak output current
- Rail-to-rail output voltage
- 120 ns maximum propagation delay
- Under Voltage Lock-Out protection (UVLO) with hysteresis
- Wide operating range: 10 to 30 Volts (V_{CC})
- Guaranteed performance over temperature -40°C ~ +110°C.

Applications

- Isolated IGBT/Power MOSFET gate drive
- Industrial Inverter
- AC brushless and DC motor drives
- Induction Heating



PIN DEFINITION

1. Anode

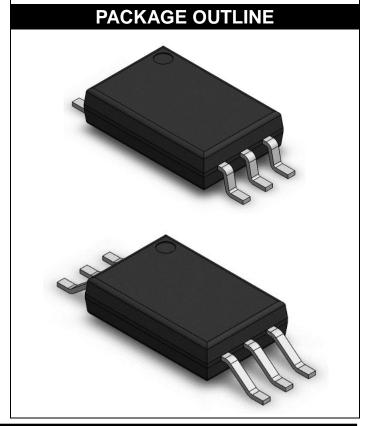
6. Vcc

2. NC

5. V_o

3. Cathode

4. GND







TRUTH TABLE						
LED	V _{CC} -V _{SS}	V _{CC} -V _{SS}	VO			
LED	(Turn-ON, +ve going)	(Turn-OFF, -ve going)	٧٥			
Off	0V to 30V	0V to 30V	Low			
On	0V to 6.9V	0V to 5.9V	Low			
On	6.9V to 8.7V	5.9V to 7.5V	Transition			
On	8.7V to 30V	7.5V to 30V	High			

Note: A 0.1µF bypass capacitor must be connected between Pin 4 and 6.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	MIN.	MAX.	UNIT	NOTE		
Storage Temperature	T _{stg}	-55	125	°C	-		
Operating Temperature	T _{opr}	-40	110	°C	-		
Output IC Junction Temperature	TJ	-	125	°C	-		
Total Output Supply Voltage	(Vcc -Vss)	0	35	V	-		
Average Forward Input Current	l _F	-	20	mA	-		
Reverse Input Voltage	V_R	-	5	V	•		
"High" Peak Output Current	I _{OH(PEAK)}	-	0.8	А	1		
"Low" Peak Output Current	I _{OL(PEAK)}	-	0.8	А	1		
Output Voltage	V _{O(PEAK)}	-0.5	Vcc	V	•		
Power Dissipation	Pı	-	45	mW			
Output IC Power Dissipation	Po	-	250	mW			
Lead Solder Temperature	T _{sol}	-	260	°C	-		

Note: Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

Note 1: Exponential waveform. Pulse width ≤ 10 µs, f ≤ 15 kHz

RECOMMENDED OPERATION CONDITIONS						
PARAMETER	SYMBOL	MIN.	MAX.	UNIT		
Operating Temperature	TA	-40	110	°C		
Supply Voltage	Vcc	10	30	V		
Input Current (ON)	I _{F(ON)}	6	16	mA		
Input Voltage (OFF)	V _{F(OFF)}	0	0.8	V		



LSOP6, DC Input, 0.8A Gate Driver Optocoupler

EL	ECTRIC	CAL OF	TICAL	CHAR	ACTER	ISTICS	
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
		INPUT	CHARAC	TERISTIC	cs		
Forward Voltage	V _F	1.6	1.9	2.4	V	I _F = 10 mA	-
Input Forward Voltage Temperature Coefficient	ΔV _F / ΔΤ	-	-1.237	-	mV/°C	IF=10mA	-
Input Reverse Voltage	BV _R	5	-	-	V	IR = 10μA	-
Input Threshold Current (Low to High)	I _{FLH}	-	1.0	5	mA	V _O > 5V, I _O = 0A	-
Input Threshold Voltage (High to Low)	V _{FHL}	0.8	-	-	V	VCC = 30 V, VO < 5V	-
Input Capacitance	C _{IN}	-	60	-	pF	VF = 0, f = 1MHz	1
		OUTPL	JT CHARA	CTERIST	ICS		
High Level Supply Current	Іссн	-	2	3	mA	$I_F = 10 \text{ mA}, V_{CC} = 30 \text{ V},$ $V_O = \text{Open}$	-
Low Level Supply Current	I _{CCL}	-	2.6	3.5	mA	$I_F = 0$ mA, $V_{CC} = 30$ V, $V_O = Open$	-
High Level Output Voltage	Vон	Vcc-1.0V	Vcc-0.4V	-	V	I _F = 10 mA, I _O = -100 mA	2,3
Low Level Output Voltage	V _{OL}	-	0.25	1	V	$I_F = 0 \text{ mA}, I_O = 100 \text{ mA}$	
High Level Output Current	Іон		-	-0.8	А	I _F = 10 mA, V _{CC} = 30V V _O = V _{CC} - 6	1
Low Level Output Current	loL	0.8	-	-	А	$I_F = 0 \text{ mA}, V_{CC} = 30V$ $V_O = V_{SS} + 6$	1
Under Voltage Lockout	V _{UVLO+}	6.9	7.9	8.7	V	Vo > 5V, I _F = 10 mA	-
Threshold	V _{UVLO} -	5.9	6.8	7.5	V	Vo < 5V, I _F = 10 mA	-

Note: All Typical values at $T_A = 25^{\circ}\text{C}$ and $V_{CC} - V_{SS} = 30 \text{ V}$, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Maximum pulse width = $10 \mu s$.

Note 2: In this test VOH is measured with a dc load current. When driving capacitive loads, VOH will approach VCC as IOH approaches zero amps.

Note 3: Maximum pulse width = 1 ms.



LSOP6, DC Input, 0.8A Gate Driver Optocoupler

	SW	/ITCHIN	IG SPE	CIFICA	TION			
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE	
	SWITCHING CHARACTERISTICS							
Propagation Delay Time	t_{PHL}	_	60	120	ns		_	
to Output Low Level	VI III			120	1.0			
Propagation Delay Time	t _{PLH}	_	55	120	ns	Rg = 47Ω ,		
to Output High Level	(PLN		33	120	113	Cg = 3 nF,		
Pulse Width Distortion	P_{WD}	_	5	80	ns	f = 10kHz,	_	
	- 115					Duty Cycle = 50%		
Propagation Delay Difference	P _{DD}	-100	_	+100	ns	$I_F = 10mA$,	_	
Between Any Two Parts	(t _{PHL} - t _{PLH})					$V_{CC} = 30V$		
Rise Time	tr	-	6	-	ns		-	
Fall Time	t _f	-	5	-	ns		-	
						I _F =7 to 16mA		
Common Mode Transient	СМн	10	35	-	kV/µs	$V_{CC}=30V$,	1,2	
Immunity at Logic High	CIVIH					T _A = 25 °C,		
						V _{CM} = 1kV		
Common Mode Transient Immunity at Logic Low		10				I _F =0mA	1,3	
	CML		35	-	kV/µs	Vcc= 30V,		
						T _A = 25 °C,		
						V _{CM} = 1kV		

Note: All Typical values at $T_A = 25$ °C and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Pin 2 needs to be connected to LED common.

Note 2: Common mode transient immunity in the high state is the maximum tolerable dVCM/dt of the common mode pulse, V_{CM} , to assure that the output will remain in the high state (meaning $V_O > 10.0V$).

Note 3: Common mode transient immunity in a low state is the maximum tolerable dVCM/dt of the common mode pulse, V_{CM} , to assure that the output will remain in a low state (meaning $V_O < 1.0V$).



LSOP6, DC Input, 0.8A Gate Driver Optocoupler

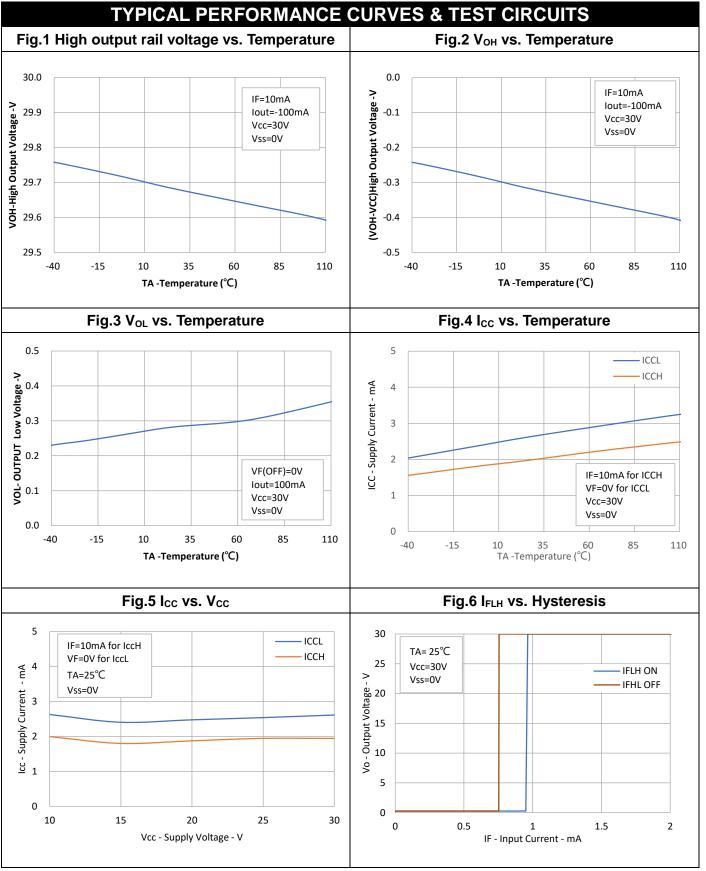
ISOLATION CHARACTERISTIC								
Parameter	Symbol	Device	Min.	Тур.	Max.	Unit	Test Condition	Note
Withstand Insulation	V _{ISO}	MPCS-5701P	5000			V	RH ≤ 40%-60%,	4.0
Test Voltage	VISO	MPCS-5701W 5000		- -	-	V	t = 1min, T _A = 25 °C	1,2
Input-Output	R _{I-O}			10 ¹²		Ω	V _{I-O} = 500V DC	1
Resistance	KI-0	-	-	10,5	_	12	VI-0 = 500 V DC	

Note: All Typical values at $T_A = 25^{\circ}\text{C}$ and $V_{CC} - V_{SS} = 30 \text{ V}$, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Device is considered a two terminal device: pins 1, 2, 3 are shorted together and pins 4, 5, 6 are shorted together.

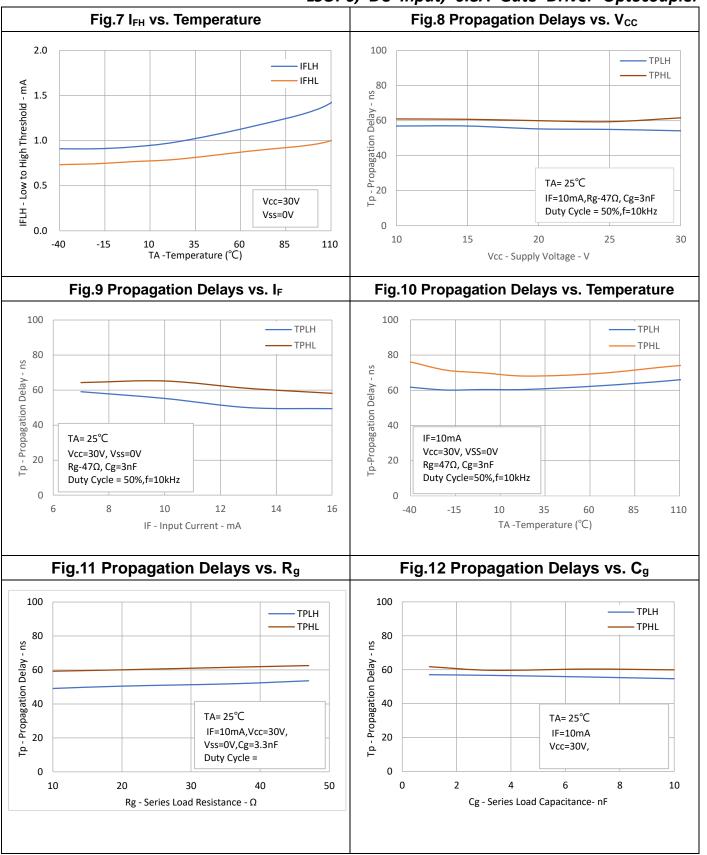
Note 2: According to UL1577, each photocoupler is tested by applying an insulation test voltage 6000VRMS for one second.







LSOP6, DC Input, 0.8A Gate Driver Optocoupler





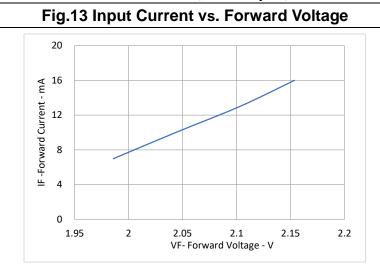


Fig.14 Iон Test Circuit

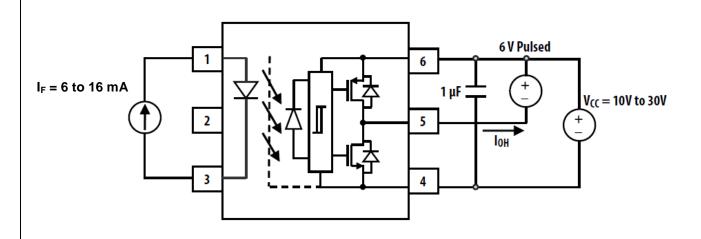
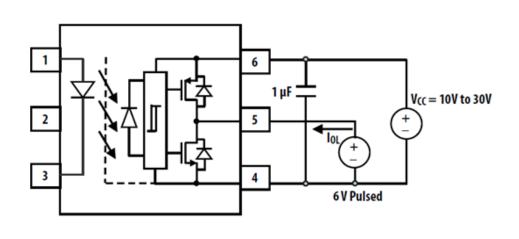
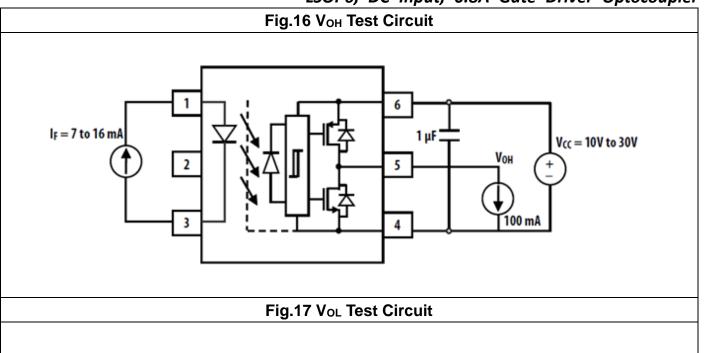


Fig.15 IoL Test Circuit







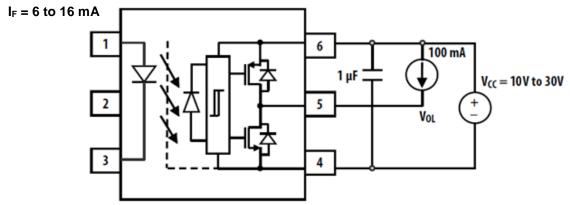


Fig.18 I_{FLH} Test Circuit

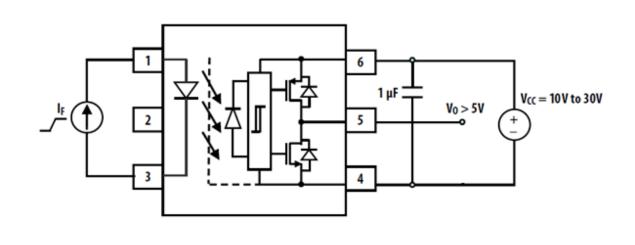




Fig.19 U_{VLO} Test Circuit I_F = 6 to 16 mA 2 4 V_{CC}

Fig.20 tphL, tpLH, tr and tf Test Circuit and Waveforms

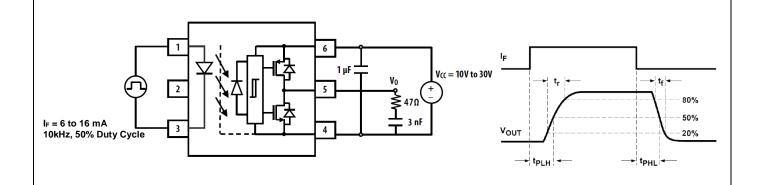
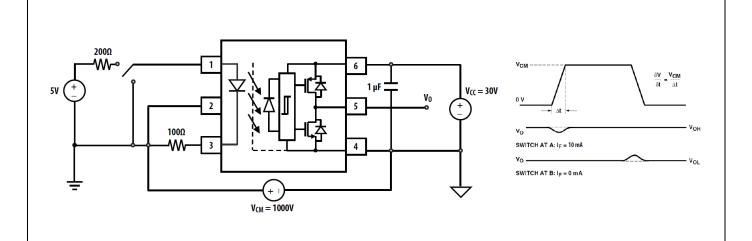


Fig.21 CMR Test Circuit with Split Resistors Network and Waveforms





Тур.0.20

Тур.0.75

LSOP6, DC Input, 0.8A Gate Driver Optocoupler PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated) Surface Mount Lead Forming (P Type) H +- 4.50±0.20 11 6.81±0.20 7.70±0.30 1.80±0.10 Typ.0.20 . Тур.2.00 Typ.0.20 Typ.0.40 Typ.0.95 Typ.1.27 9.70±0.30 General Tolerance: +/-0.25mm Surface Mount (Gullwing) Lead Forming (W Type) 1.50±0.20 6.81±0.20 7.70±0.30 1.80±0.10 Typ.0.20 Typ.2.00

11.50±0.30

Rev: 2.1 Release Date: 2024/7/29

Typ.0.40

Typ.1.27

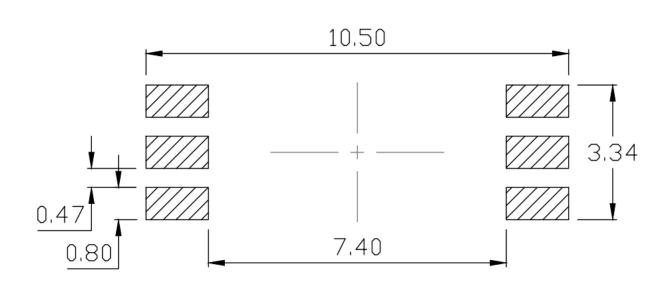
General Tolerance: +/-0.25mm



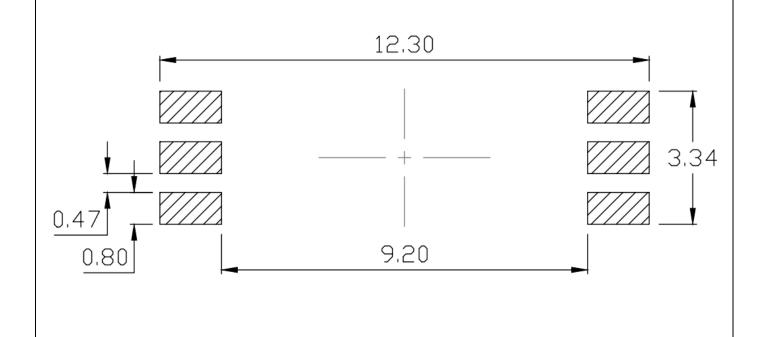


RECOMMENDED SOLDER MASK (Dimensions in mm unless otherwise stated)

Surface Mount Lead Forming (P Type)



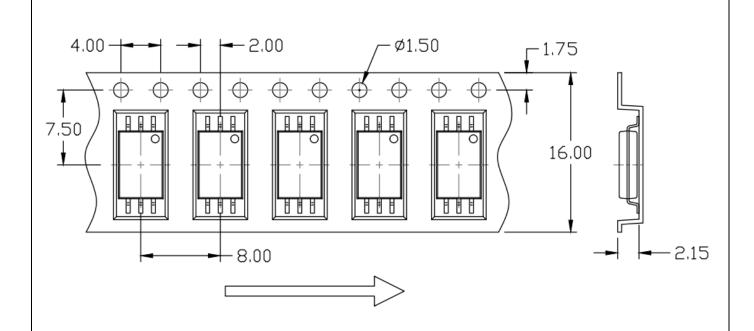
Surface Mount (Gullwing) Lead Forming (W Type)



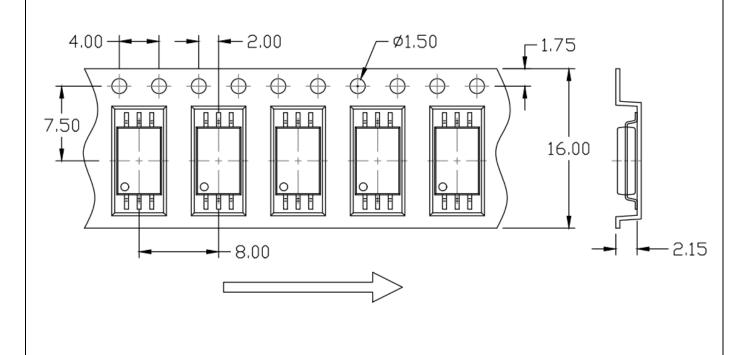


CARRIER TAPE SPECIFICATIONS (Dimensions in mm unless otherwise stated)

Surface Mount Lead Forming (P Type) Option T1



Surface Mount Lead Forming (P Type) Option T2

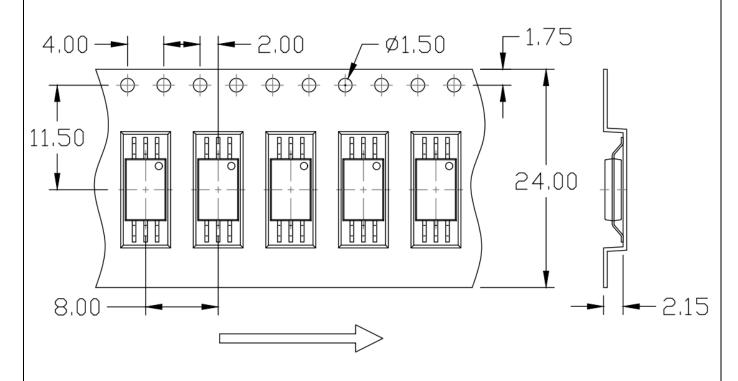




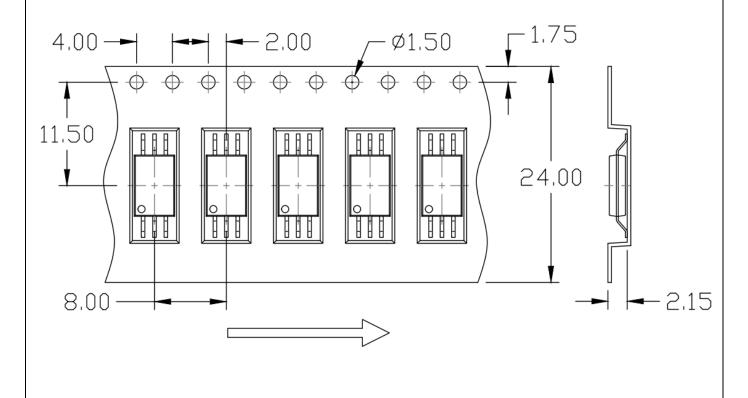


CARRIER TAPE SPECIFICATIONS (Dimensions in mm unless otherwise stated)

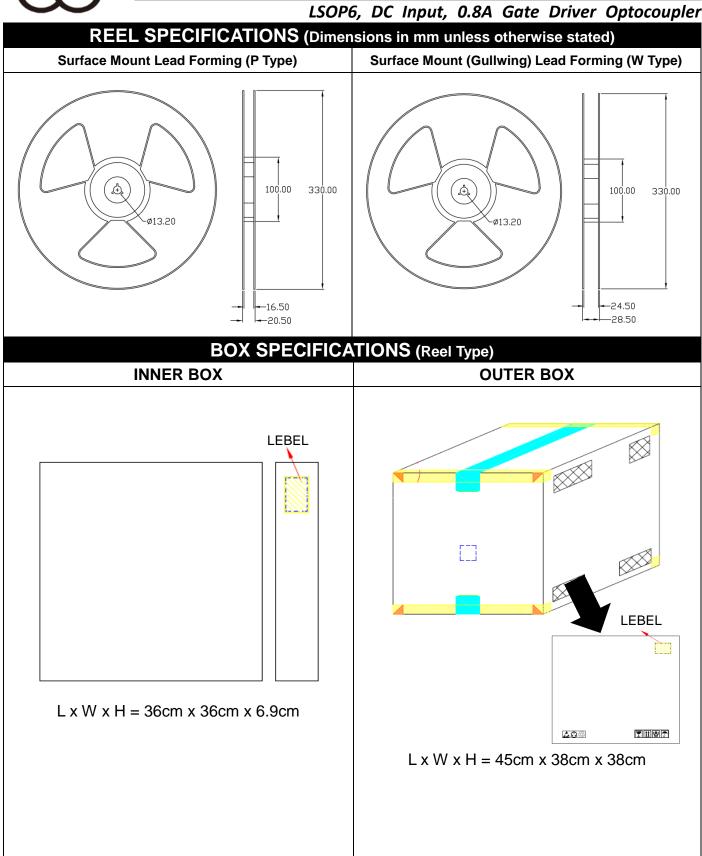
Surface Mount (Gullwing) Lead Forming (W Type) Option T1



Surface Mount (Gullwing) Lead Forming (W Type) Option T2









LSOP6, DC Input, 0.8A Gate Driver Optocoupler

ORDERING AND MARKING INFORMATION

MARKING INFORMATION



M : Company Abbr.YY : Year date codeWW : 2-digit work week

5701 : Part Number

T or H : Factory identification mark
V : VDE Identification(Option)

ORDERING INFORMATION

MPCS-5701(P/W)-ZV

MPC - Company Abbr.

S – Stack

5701 - Part Number

P/W – Lead Form Option

(P-9mm Clearance or W-11mm Clearance)

Z – Tape and Reel Option (T1/T2)

V –VDE Option (V or None)

LABEL INFORMATION



喆光照明光電股份有限公司 WISELITE Optronics Co., Ltd

Part No: XXXXXXXXXXXXX Bin Code: X



Date Code : XXXX Q'ty : XXXX pcs





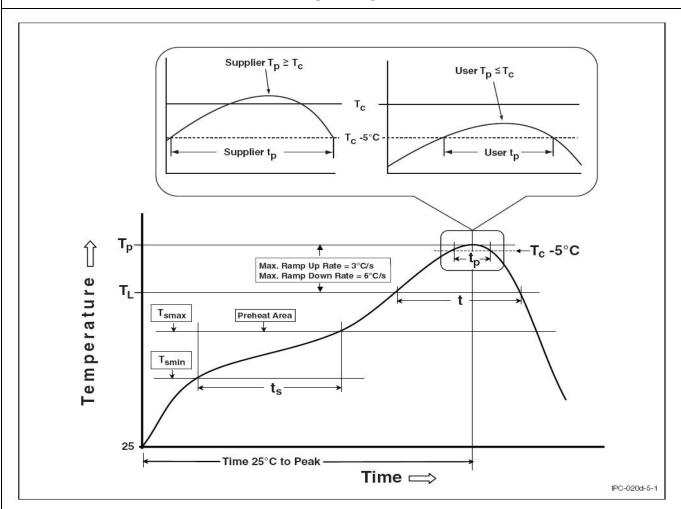
PACKING QUANTITY

Option Quantity		Quantity – Inner box	Quantity – Outer box
Option P T1/T2	3000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 45k Units
Option W T1/T2	3000 Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 30k Units



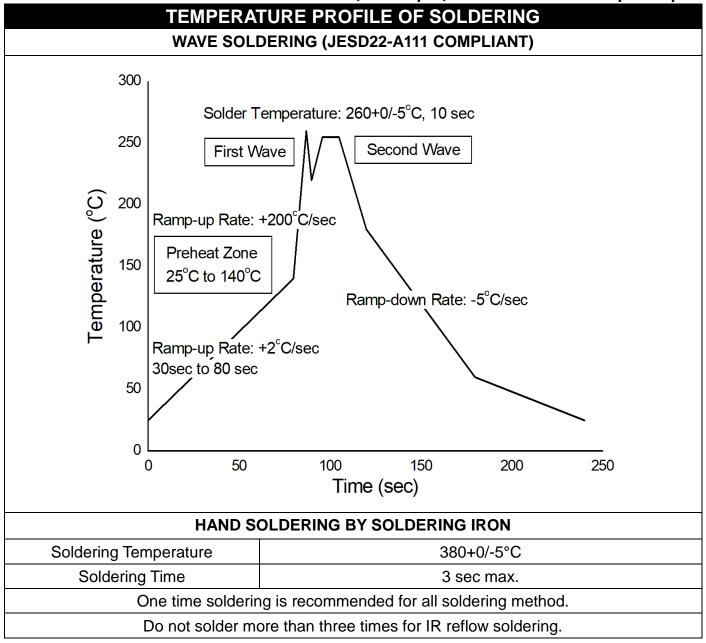
REFLOW INFORMATION

REFLOW PROFILE



Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile	
Temperature Min. (Tsmin)	100°C	150°C	
Temperature Max. (Tsmax)	150°C	200°C	
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds	
Ramp-up Rate (tL to tP)	3°C/second max.	3°C/second max.	
Liquidous Temperature (TL)	183°C	217°C	
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds	
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C	
Time (tP) within 5°C of 260°C	20 seconds	30 seconds	
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max	
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.	









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