

LSOP6, DC Input, 4.0A Gate Driver Optocoupler

Description

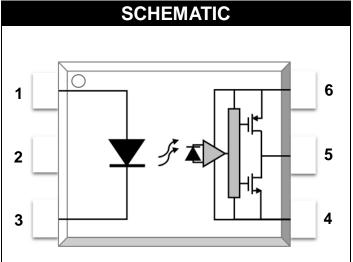
The MPCS-343 U series Photocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control inverter applications and inverters in power supply system. It contains an LED optically coupled to an integrated circuit with a power output stage. The 4.0A peak output current is capable of directly driving most IGBTs with ratings up to 1200 V/200 A. For IGBTs with higher ratings, the MPCS-343 U series can be used to drive a discrete power stage which drives the IGBT gate.

Features

- 4.0 A maximum peak output current
- Rail-to-rail output voltage
- 110 ns maximum propagation delay
- Under Voltage Lock-Out protection (UVLO) with hysteresis
- Wide operating range: 10 to 30 Volts (V_{cc})
- Guaranteed performance over temperature -40°C ~ +110°C.
- Regulatory Approvals
 - UL UL1577
 - VDE EN60747-5-5(VDE0884-5)
 - CQC GB4943.1, GB8898

Applications

- IGBT/MOSFET gate drive
- Uninterruptible power supply (UPS)
- Industrial Inverter
- AC/Brushless DC motor drives
- Switching power suppliers



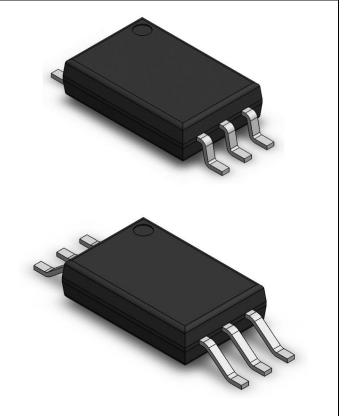
PIN DEFINITION

1. Anode 6. Vcc

2. NC

- 5. Vo
- 3. Cathode 4. GND

PACKAGE OUTLINE



Release Date: 2024/7/29



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TRUTH TABLE						
LED	V _{CC} -V _{SS}	V _{CC} -V _{SS}	VO			
	(Turn-ON, +ve going)	(Turn-OFF, -ve going)	vu			
OFF	0 - 30 V	0 - 30 V	Low			
ON	0-6.9 V	0-5.9 V	Low			
ON	6.9 – 8.7 V	5.9 – 7.5 V	Transition			
ON	8.7 - 30 V	7.5 - 30 V	High			

Note: A 0.1μ F bypass capacitor must be connected between Pin 4 and 6.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	MIN.	MAX.	UNIT	NOTE	
Storage Temperature	T _{stg}	-55	125	°C	-	
Operating Temperature	T _{opr}	-40	110	°C	-	
Output IC Junction Temperature	TJ	-	125	°C	-	
Total Output Supply Voltage	(Vcc –Vss)	0	35	V	-	
Average Forward Input Current	lF	-	20	mA	-	
Reverse Input Voltage	V _R	-	5	V	-	
"High" Peak Output Current	Юн(реак)	-	4.0	А	1	
"Low" Peak Output Current	IOL(PEAK)	-	4.0	А	1	
Output Voltage	V _{O(PEAK)}	-0.5	Vcc	V	-	
Power Dissipation	Pi	-	45	mW	-	
Output IC Power Dissipation	Po	-	700	mW	-	
Lead Solder Temperature	T _{sol}	-	260	°C	-	

Note: Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

Note 1: Exponential waveform. Pulse width \leq 10 µs, f \leq 15 kHz

RECOMMENDED OPERATION CONDITIONS							
PARAMETER	SYMBOL	MIN.	MAX.	UNIT			
Operating Temperature	TA	-40	110	°C			
Supply Voltage	V _{CC}	10	30	V			
Input Current (ON)	IF(ON)	5	16	mA			
Input Voltage (OFF)	Vf(Off)	-3.0	0.8	V			



	ECTRIC/		-	-	-	Gate Driver Optoc STICS	oupler
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
		INPUT C	HARACT	ERISTICS	5	I	
Input Forward Voltage	VF	1.6	1.9	2.4	V	IF=10mA	-
Input Forward Voltage Temperature Coefficient	ΔV _F / ΔΤ	-	-1.237	-	mV/°C	IF=10mA	-
Input Reverse Voltage	BV _R	5	-	-	V	IR = 10µA	-
Input Threshold Current (Low to High)	I _{FLH}	-	0.9	2	mA	$V_{\rm O} > 5V, I_{\rm O} = 0A$	-
Input Threshold Voltage (High to Low)	VFHL	0.8	-	-	V	VCC = 30 V, VO < 5V	-
Input Capacitance	CIN	-	60	-	pF	f = 1 MHz, VF = 0 V	-
		OUTPUT	CHARAC	TERISTIC	S		
High Level Supply Current	Іссн	-	1.70	3	mA	IF = 10 mA, VCC = 30V, VO = Open	-
Low Level Supply Current	I _{CCL}	-	2.11	3	mA	$I_F = 0 \text{ mA}, \text{VCC} = 30\text{V},$ VO = Open	-
High level output current	Іон	4.0	-	-	A	I _F = 10 mA, VCC = 30V VO = VCC - 15	1
Low level output current	Ιοι	4.0	-	-	A	IF = 0 mA, VCC = 30V VO = VSS + 15	1
High level output voltage	Vон	29.7	29.88	-	V	IF = 10mA, IO = -100mA	2,3
Low level output voltage	Vol	-	0.1	0.3	V	I⊧ = 0 mA, IO = 100 mA	-
UVLO Threshold	Vuvlo+	6.9	7.9	8.7	V	VO > 5V, IF = 10 mA	-
	Vuvlo-	5.9	6.8	7.5	V	VO < 5V, IF = 10 mA	-

Note: All Typical values at $T_A = 25^{\circ}C$ and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Maximum pulse width = $10 \ \mu s$.

Note 2: In this test VOH is measured with a dc load current. When driving capacitive loads, VOH will approach VCC as IOH approaches zero amps.

Note 3: Maximum pulse width = 1 ms.



Driver Onto

4 NA Gate

		<u></u>	1PO, DU	. mput,	4.0A	Gate Driver Optoc	oupier
	SWIT	CHING	SPEC		ΓΙΟΝ		
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
Propagation Delay Time to Low Output Level	tрін	-	61.3	110		Rg = 10 Ω, Cg = 25 nF,	-
Propagation Delay Time to High Output Level	t _{PHL}	-	70.0	110			-
Pulse Width Distortion	Pwd	-	22	70	ns	f = 10kHz, Duty Cycle = 50%	-
Propagation Delay Difference Between Any Two Parts	Pdd (tphl - tplh)	-100	-	+100		IF = 10mA, VCC = 30V	-
Output Rise Time (20 to 80%)	tr	-	20	-		VCC - 30V	-
Output Fall Time (80 to 20%)	t _f	-	15	-			-
Common mode transient immunity at high level output	CM⊦	20	40	-	kV/µs	IF= 7 to 16mA VCC= 30V, TA= 25 °C, VCM= 1kV	1,2
Common mode transient immunity at low level output	CM∟	20	40	-	kV/µs	IF=0mA VCC= 30V, TA= 25 °C, VCM= 1kV	1,3

ISOP6 DC Innut

Note: All Typical values at TA = 25° C and V_{CC} – V_{SS} = 30 V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Pin 2 needs to be connected to LED common.

Note 2: Common mode transient immunity in the high state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in the high state (meaning VO > 15.0V). Note 3: Common mode transient immunity in a low state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in a low state (meaning VO < 1.0V).



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ISOLATION CHARACTERISTIC								
PARAMETER	SYMBOL	DEVICE	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
Withstand Insulation	Vier	MPCS-343P	5000			V	RH ≤ 40%-60%,	1.2
Test Voltage	Viso	MPCS-343W	5000	-	-	v	t = 1min, T _A = 25 °C	1,2
Input-Output	RI-0			10 ¹²		Ω	V _{I-0} = 500V DC	1
Resistance	NI-0	-	-	10	-	52	$v_{1-0} = 500 v DC$	

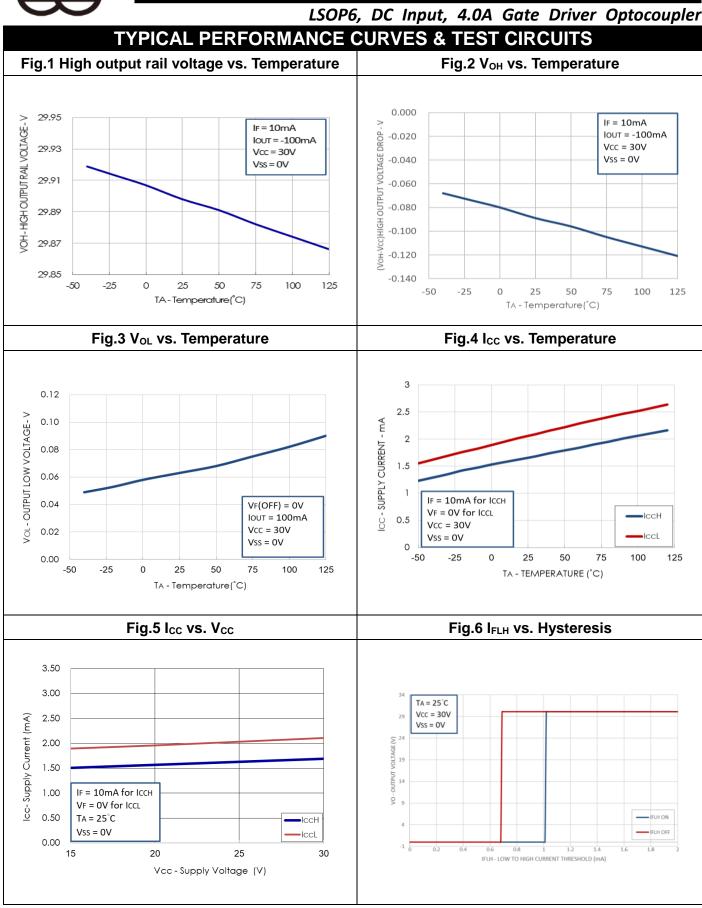
Note: All Typical values at $T_A = 25^{\circ}$ C and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Device is considered a two terminal device: pins 1, 2, 3 are shorted together and pins 4, 5, 6 are shorted together.

Note 2: According to UL1577, each photocoupler is tested by applying an insulation test voltage

6000VRMS for one second. This test is performed before the 100% production test for partial discharge.



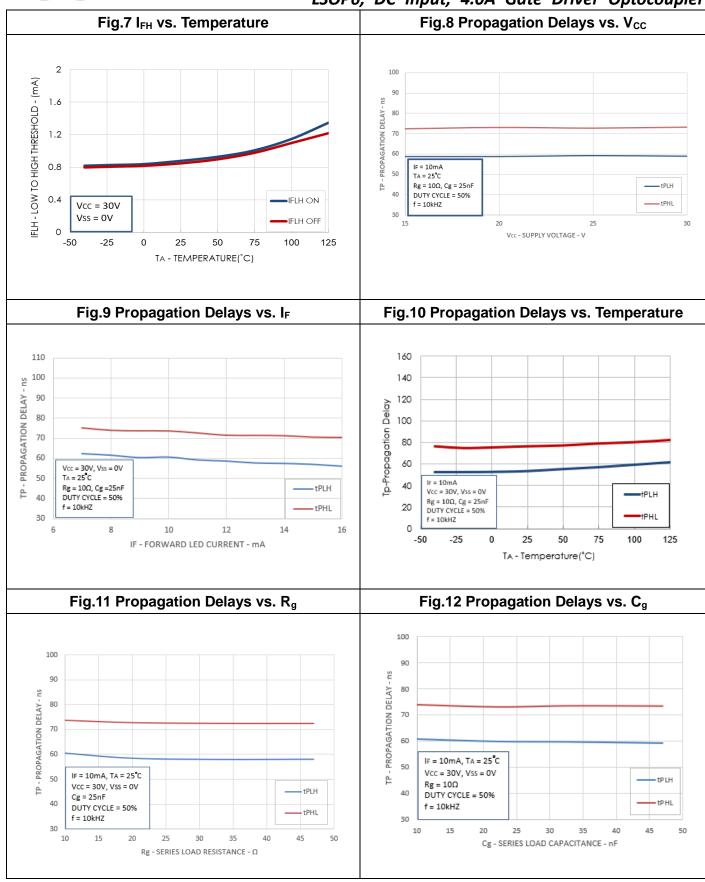


Rev: 2.1

Release Date: 2024/7/29

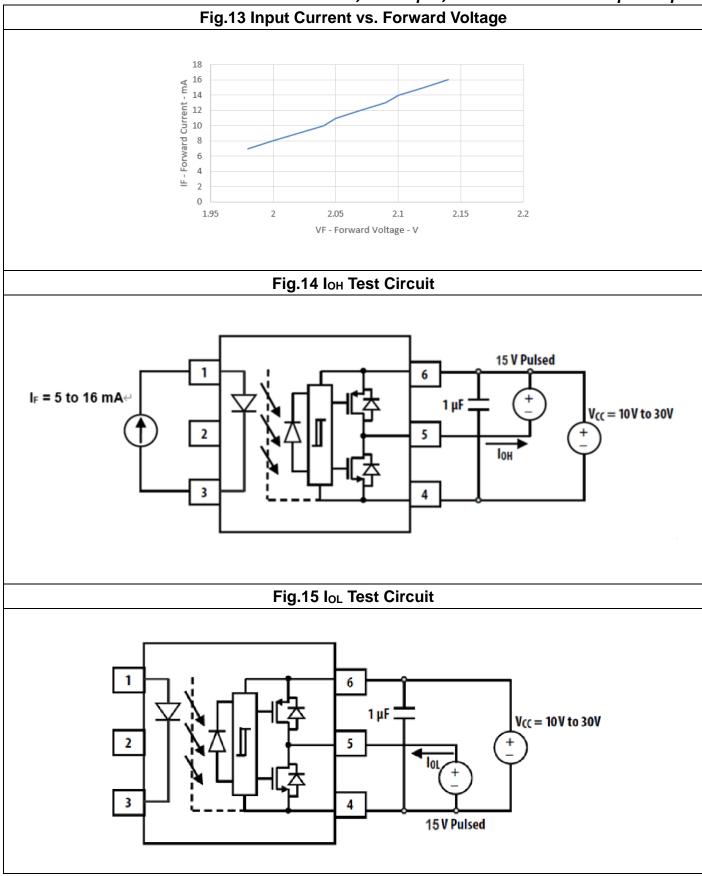


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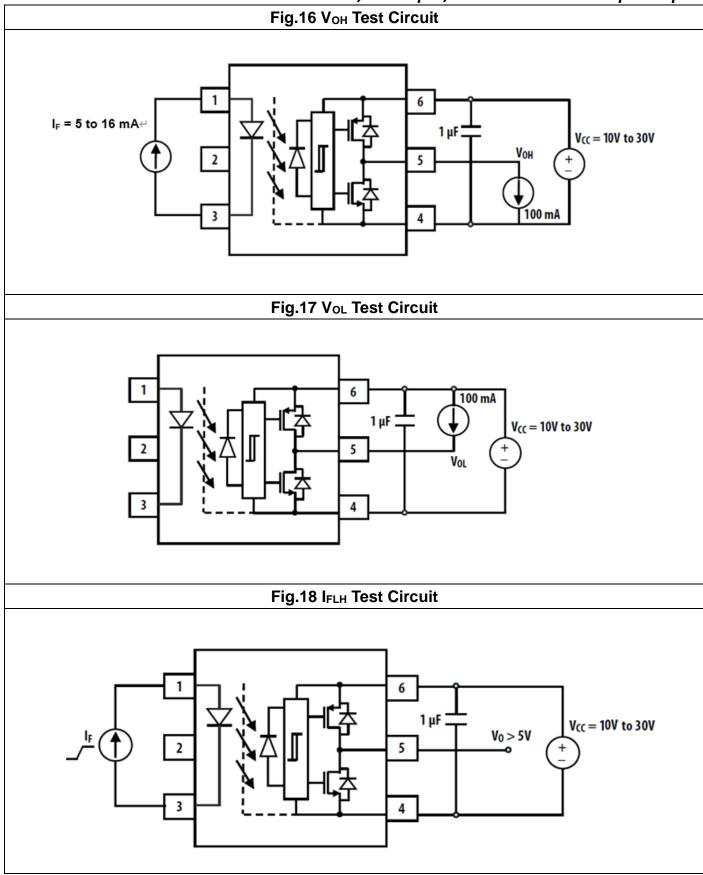


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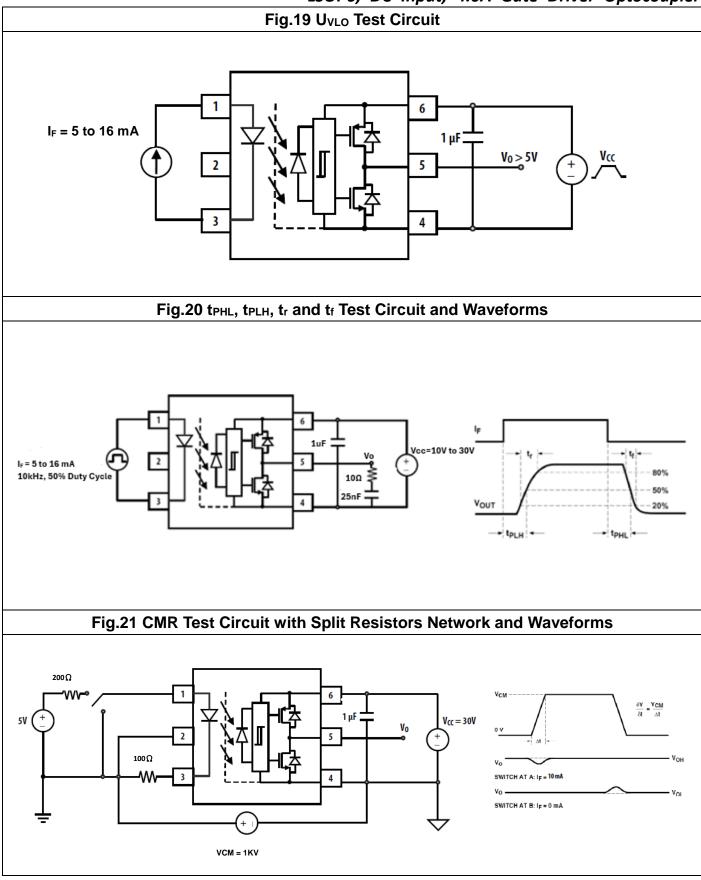




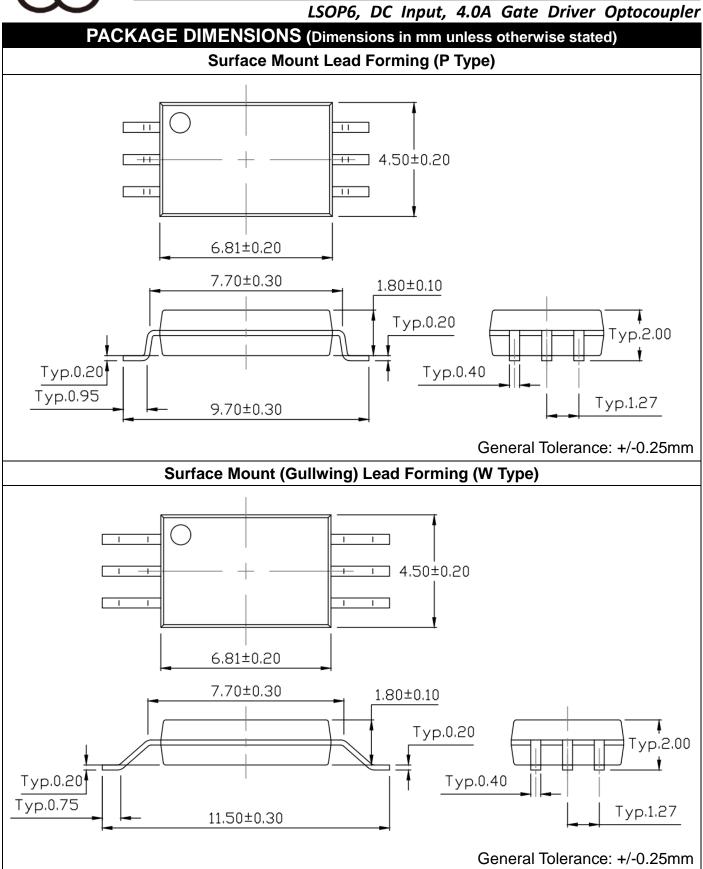




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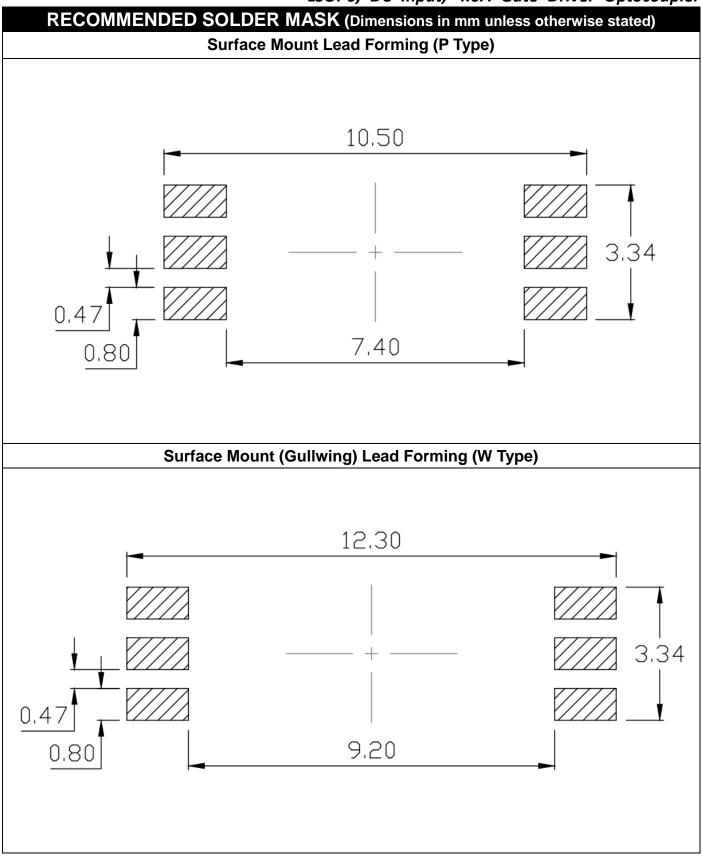




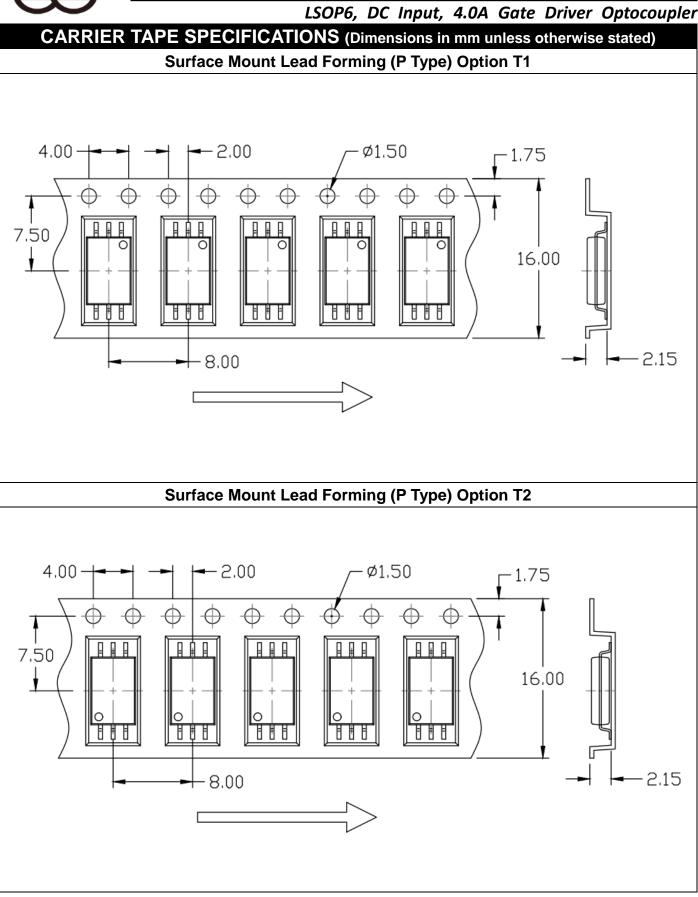




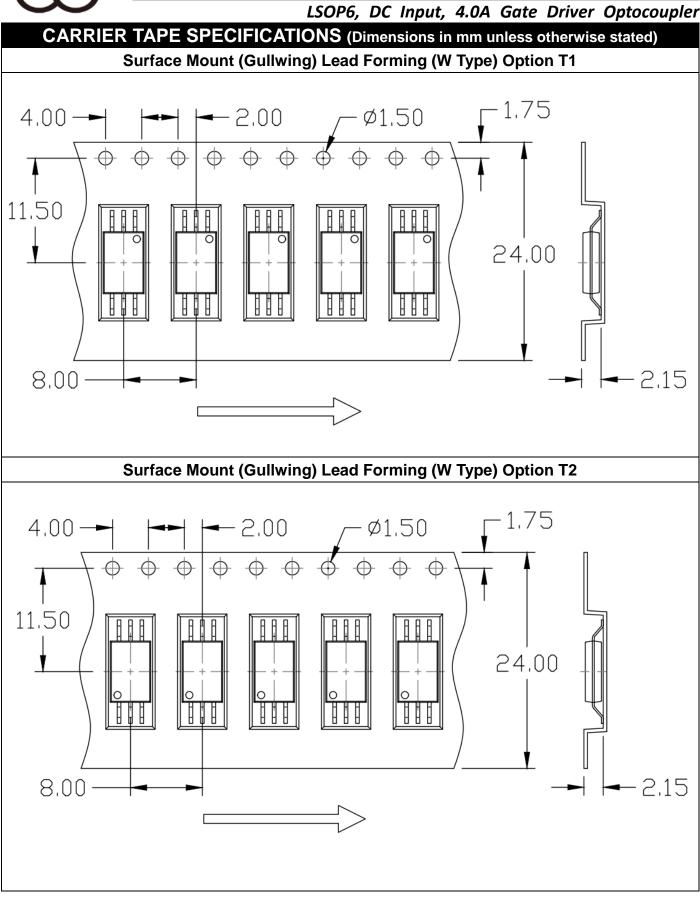




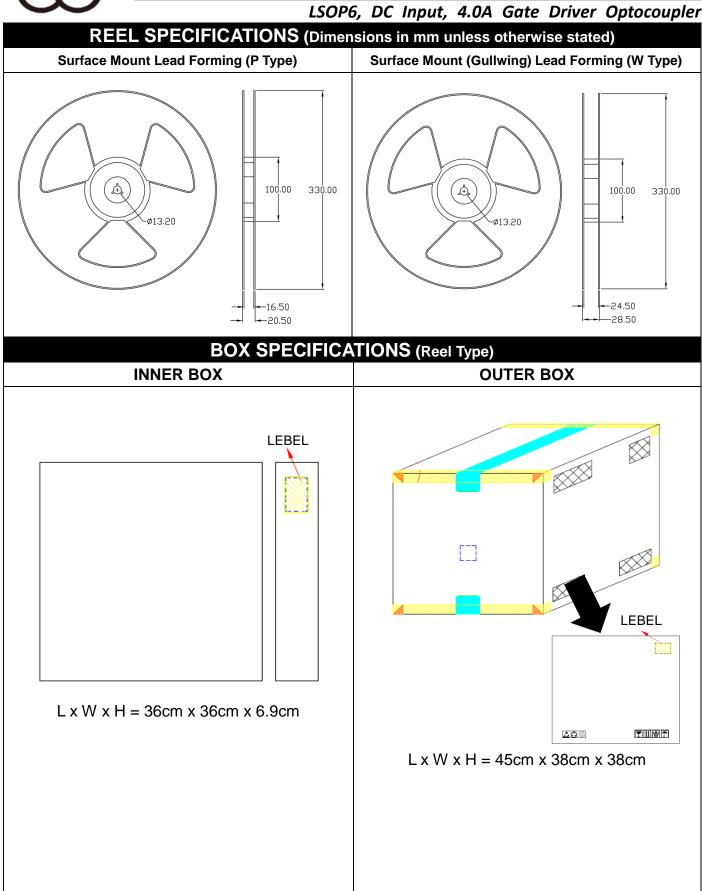














LSOP6, I	DC Input, 4.0A Gate Driver Optocoupler						
ORDERING AND MARKING INFORMATION							
MARKING INFO	RMATION						
MYYWW 343 TV U	 M : Company Abbr. YY : Year date code WW : 2-digit work week 343 : Part Number T or H : Factory identification mark V : VDE Identification(Option) U : V_{cc} 10-30V(Option) 						
ORDERING INFORMATION	LABEL INFORMATION						
MPCS-343(P/W)-ZV-U							
$\begin{array}{l} \mbox{MPC}-\mbox{Company Abbr.} \\ \mbox{S}-\mbox{Stack} \\ \mbox{343}-\mbox{Part Number} \\ \mbox{P/W}-\mbox{Lead Form Option} \\ \mbox{(P-9mm Clearance or W-11mm Clearance)} \\ \mbox{Z}-\mbox{Tape and Reel Option (T1/T2)} \\ \mbox{V}-\mbox{VDE Option (V or None)} \\ \mbox{U}-\mbox{V}_{CC}\mbox{10-30V Option} \end{array}$	■						
PACKING QU	PACKING QUANTITY						

PACKING QUANTITY							
Option	Quantity	Quantity – Inner box	Quantity – Outer box				
Option P T1/T2	3000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 45k Units				
Option W T1/T2	3000 Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 30k Units				



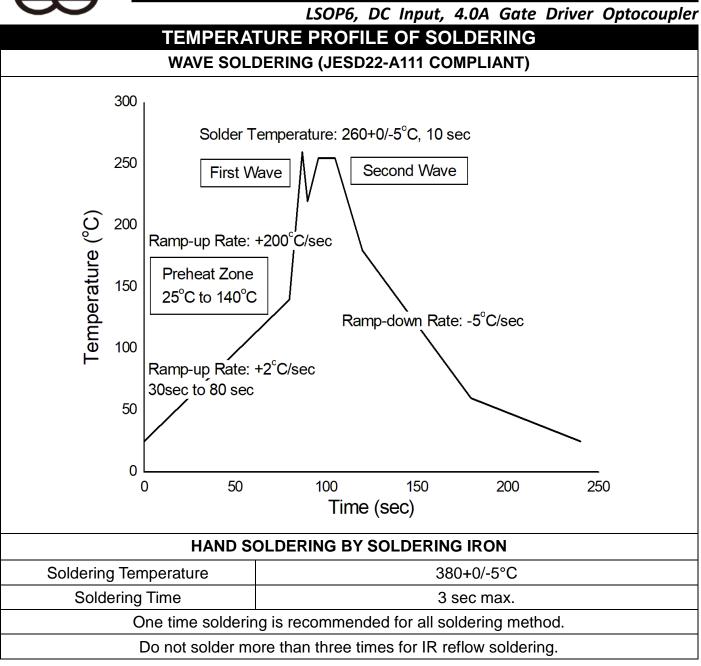
LSOP6, DC Input, 4.0A Gate Driver Optocoupler **REFLOW INFORMATION REFLOW PROFILE** Supplier $T_p \ge T_c$ User T_p ≤ T_c Tc T_c -5°C Supplier t_p Tp T_c -5°C Temperature 📺 Max. Ramp Up Rate = 3°C/s Max. Ramp Down Rate = 6°C/s T_{L} T_{smax} Preheat Area 1 T_{smin} 25 Time 25°C to Peak Time ⇒ IPC-020d-5-1 **Profile Feature Sn-Pb Assembly Profile Pb-Free Assembly Profile** Temperature Min. (Tsmin) 100°C 150°C 150°C 200°C Temperature Max. (Tsmax) Time (ts) from (Tsmin to Tsmax) 60-120 seconds 60-120 seconds 3°C/second max. Ramp-up Rate (tL to tP) 3°C/second max. Liquidous Temperature (TL) 183°C 217°C Time (tL) Maintained Above (TL) 60 - 150 seconds 60 - 150 seconds 235°C +0°C / -5°C 260°C +0°C / -5°C Peak Body Package Temperature Time (tP) within 5°C of 260°C 20 seconds 30 seconds Ramp-down Rate (TP to TL) 6°C/second max 6°C/second max

8 minutes max.

6 minutes max.

Time 25°C to Peak Temperature







LSOP6, DC Input, 4.0A Gate Driver Optocoupler DISCLAIMER

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- This product is not intended to be used for military, aircraft, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Please contact WISELITE sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify WISELITE's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.