

MCU Pin	AFS / ANA				Function	PCB Default Function	PCB Option Function	Note	PI
PA0	ADC_I0				OPA	OP0_P0	(option)	FIB Pin	Q32
PA1	ADC_I1				Key Board	KEY_IN			Q32
PA2	ADC_I2		CAN_TX		MX	MX_ANA			Q32
PA3	ADC_I3		CAN_RX		VR	VR_IN			Q32
PA4	OP0_I0				OPA	OP0_I0		OP0_I0	Q64
PA5	OP0_I1					OP0_I1		OP0_I1	Q64
PA6	OP0_P0					OP0_P0	(option)	LCD IO/OPA error	Q64
PA7	GPIO				OPA	OP0_EN			Q64
PA8	ASB_P0	SPI0_NSS			ARGB	ASB_P0		CMP0_I0	•
PA9		SPI0_MISO						CMP0_I1	Q32
PA10		SPI0_CLK	TM26_IC0					CMP1_I0	•
PA11		SPI0_MOSI	TM26_IC1					CMP1_I1	Q32
PA12	TM36_IC0				Shuttle	Shuttle_A		CMP2_I0	Q48
PA13	TM36_IC1					Shuttle_B		CMP2_I1	Q48
PA14	GPIO					Shuttle_S1		CMP3_I0	Q48
PA15	GPIO	ADC_I15			SPI LCD	LCD_BL		CMP3_I1	Q48
PB0	URT1_NSS	SPI0_NSS	I2C1_SCL			LCD_CS		URT6_TX	•
PB1	URT1_RX	SPI0_MISO	I2C1_SDA			LCD_SDI		URT6_RX	•
PB2	URT1_CLK	SPI0_CLK	I2C0_SDA	URT2_TX		LCD_SCK		URT7_TX	•
PB3	URT1_TX	SPI0_MOSI	I2C0_SCL	URT2_RX		LCD_SDO		URT7_RX	•
PB8	URT0_TX				Debug UART	PC_TXD			•
PB9	URT0_RX					PC_RXD			•
PB10	GPIO	I2C0_SCL			SPI LCD	LCD_RSTN			•
PB11	GPIO	I2C0_SDA				LCD_RS			•
PB13	GPIO				BLE	BLE_EN			Q48
PB14	GPIO					BLE_STA			Q48
PC0	URT5_TX	I2C0_SCL	URT5_TX			BLE_TX		ICKO, TM36_IC0	•
PC1	URT5_RX	I2C0_SDA	URT5_RX			BLE_RX			•
PC2	MF_S0		MF_S0		MX	MX_STB	(option)		Q48
PC3	GPIO		MF_S1			MX_DET	(option)		Q48
PC4	SWCLK				SWD			SWCLK	•
PC5	SWDO							SWDIO	•
PC6	SWSRST							RSTN	•
PC8	GPIO	I2C0_SCL	URT6_TX		MX	MX_TX2	(option)	URT1_TX	Q32
PC9	GPIO	I2C0_SDA	URT6_RX			MX_RX2	(option)	URT1_RX	Q32
PC10	CAN_TX	I2C1_SCL	URT7_TX	URT0_TX		MX_TX		URT1/2_TX	Q48
PC11	CAN_RX	I2C1_SDA	URT7_RX	URT0_RX		MX_RX		URT1/2_RX	Q48
PC12									Q48
PC13	GPIO	XIN	URT2_RX		LED Driver	TS_LED0		URT6_RX	•
PC14	GPIO	XOUT	URT2_TX			TS_LED1		URT6_TX	•
PD0	SPI0_NSS	URT2_NSS			SPI Flash	FS_CEN		CS#	Q32
PD1	SPI0_CLK	URT2_CLK				FS_D1		SPI0_CLK	Q32
PD2	SPI0_MOSI	URT2_TX				FS_SCK		SPI0_MOSI	Q32
PD3	SPI0_MISO	URT2_RX				FS_D0		SPI0_D3	Q48
PD7	GPIO				CFG	TS_CFG0		SPI0_D4	Q32
PD8	GPIO					TS_CFG1		SPI0_D7	Q48
PD9	GPIO					TS_CFG2		SPI0_D6	Q48
PD10	GPIO					TS_CFG3		SPI0_D5	Q48

