



笙泉科技股份有限公司

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## **MG32F02A072/132 Series Suspension of promotion**

MEGAWIN TECHNOLOGY CO., LTD hereby formally announces the end of promotion for the MG32F02A072/132 series.

Due to limited production capacity, the MG32F02A072/132 series is no longer recommended for new design-in projects and will not be promoted to new customers. The recommended alternative is the MG32F02A(U)064/128 series.

Please advise customers to adopt the MG32F02A(U)064/128 series for new projects and to migrate existing projects from the MG32F02A072/132 series accordingly.

Item	Flash ROM	Data RAM	Max. Freq.	Timer	IO	12-Bit ADC	Comp.	Features	CCP	ISP /IAP	Package
MG32F02A064	64KB	8192B	48MHz	7+RTC	44/59	16-CH	2	UART x7, (I <sup>2</sup> C)x2 SPI/QPI/OPI, CRC32,DMA,DAC	8-CH	YES	LQFP48 LQFP64
MG32F02U064	64KB	16384B	48MHz	7+RTC	44/59	16-CH	2	UART x7, (I2C)x2 USB/SPI/QPI/OPI, CRC32,DMA,DAC	8-CH	YES	LQFP48 LQFP64
MG32F02A128	128KB	16384B	48MHz	7+RTC	59/73	16-CH	2	UART x7, (I <sup>2</sup> C)x2 SPI/QPI/OPI, CRC32,DMA,DAC	8-CH	YES	LQFP64 LQFP80
MG32F02U128	128KB	16384B	48MHz	7+RTC	59/73	16-CH	2	UART x7, (I2C)x2 USB/SPI/QPI/OPI, CRC32,DMA,DAC	8-CH	YES	LQFP64 LQFP80

\* VDD 1.8V~5.5V

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